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THE DESIGN AND APPLICATION OF A PROGRAMMABLE COULOSTATIC POLARIZER

By

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ABSTRACT

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By

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A microcomputer controlled coulostatic electrochemical instrument has been slightly modified. The design principles and applications of the instrument, the programmable coulostatic polarizer (PCP), will be discussed. The PCP consists of an electrochemical cell, a digitally controlled charge generator for adding charge to the electrode, a voltage measurement system to monitor the working electrode potential, a real time clock to control the timing of experiments, and a microcomputer system to control the operation of charge generator, voltage measurement system and real time colck.

When an electrochemical cell is controlled by a potentiostat which is generally employed in conventional instruments, an IR error which is the product of the portion

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of current and the portion of resistance sensed by the reference electrode always exists. This error can be significant if the solution resistance is high or if the current is large. The PCP is based on the coulostatic polarization method which employs the addition of charge pulses to the electrode and measures the electrode potential between charge additions when there is no current in the solution. The potentials measured with coulostatic method are thus free from IR error.

The PCP can be operated in ways analogous to many electrochemical polarization modes where the charge, current, or potential must be measured or controlled. These can be accomplished without modification of the instrument hardware.

Some applications of the PCP have been achieved as follows:

1. Fast measurement of electrode capacitance:

Capacitance measurements for mercury electrode in 1.0 M KC1 solution can be obtained within 8 seconds with 2 % RSD.

2. Electrode capacitance measurements for low concentration of electrolyte solution:

The concentration of KC1 solution for capacitance measurement on mercury electrode can be as low as 10 μ M.

3. Charging current compensation for constant

chronopotentiometry:

Compensation of charging current using blank electrolyte (KCl) solution has been performed for the solutions of T1C1 in 0.1 M KC1. A dramatic correspondence of transition times with theory is obtained. In-situ compensation of charging current allows the detection limit for cadmium to be as low as 0.25 μ M.

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CHAPTER 1

INTRODUCTION

Blectrochemical instruments are often based on the potentiostat which tries to maintain the potential of the working electrode equal to a control voltage provided by a signal generator. The control signal may have a constant value or vary with time. Figure 1-1 shows an equivalent circuit for an electrochemical cell which is controlled by a potentiostat(not shown). The solution resistance **i s** represented by R_g. The resistance of an electrochemical reaction which depends on the kinetics and the availability of electroactive species is represented by R_e. The double layer capacitance of the working electrode appears in the equivalent circuit as C_{d1}. On the solution side of the electrode/solution interface, there are ionic species and solvent dipoles. A change in the electrode potential causes a change in the distribution of ions and the orientation of solvent dipoles. Thus, the electrode/solution interface responds like a capacitor. A detailed discussion of the double layer capacitance can be found in Chapter 3. In a potentiostat, the electrochemical cell is part of a feedback loop in which the cell current is continuously controlled in order to minimize the difference between the measured value of the electrode potential and the desired (control signal)





potential. However, the actual potential of the working electrode deviates from the measured value (and thus from the desired value) whenever there is a current in the cell. This deviation is caused by the IR drop which is the product of current \underline{I} and the portion of the solution resistance \underline{R} sensed by the reference electrode. In order to reduce the IR component of the measured voltage, solvents with high dielectric constant and high concentrations of supporting electrolyte are used. In some cases, a Luggin capillary reference electrode must be used to minimize the distance between the working and reference electrodes.

When the supporting electrolyte concentration is below 0.01 M (as is often the case when nonaqueous solvents are used), the IR drop can introduce a significant error in potential control.

Some electrochemists have tried to use potentiostats with positive IR compensating feedback (1-6) to reduce the error in potential control and enhance the system response. However, it has been found that the circuit tends to oscillate (3,5,6). A common method to compensate the solution resistance is to increase the level of compensation until the potentiostat/cell system is no longer stable. It was pointed out in a review paper (7) that this can be a rather inaccurate procedure, as many systems can be unstable at rather less than 100% compensation and, conversely, some systems are stable even beyond 100% compensation.

A. Coulostatic Method

In the early 1960's P. Delahay and W. H. Reinmuth each published a series of papers in which they described the theory and applications of a new method, the coulostatic polarization method (8-12). Figure 1-2 shows an equivalent circuit of an electrochemical cell and an example of charge pulse for the coulostatic method. In the coulostatic method, a brief charge pulse (microsecond range) of q_n coulombs is injected into the cell and then the cell is kept in an open circuit condition. The quantity of charge may be controlled by applying a constant current i for a short period of time t or by injecting a known quantity of charge with a capacitor charged to a known voltage. After the charge pulse is injected, the Faradaic current if, or the charge consumed by the Faradaic process, is supplied by the charge on the double layer capacitor C_{d1}. The original coulostatic method usually involved the injection of a large charge pulse to alter the potential of an electrode from several to several hundred millivolts followed by the open-circuit monitoring of the potential. The potential/time curve can then be related to the electrode reaction processes of charge transfer, diffusion, and chemical kinetics (8,10,12). For example, for a simple redox system Red + ne⁻ \neq Ox, with both species soluble, with mass transport by semi-infinite diffusion, and with a constant double layer capacitance C_{d1} , the potential/time







relationships were derived for the following situations:

1. With a small potential perturbation ΔE (nF $\Delta E/RT$ << 1) and with fast charge transfer reaction (12) (the rate of the reaction is completely limited by the diffusion of Red to the electrode surface),

$$\Delta E = \Delta E_{i} \cdot \exp(t/\tau_{d}) \cdot \operatorname{erfc}(t/\tau_{d})^{1/3} \qquad (1-1),$$

where ΔE is the change of potential from its initial value, ΔE_i is the initial potential perturbation, t is the time after charge injection, and

$$(\tau_d)^{1/2} = (\text{RTC}_{d1}/n^2 F^2) \cdot ((C_{\text{Red}})^{-1}(D_{\text{Red}})^{-1/2} + (C_{\text{ox}})^{-1}(D_{\text{ox}})^{-1/3}) (1-2),$$

where n is the number of electrons transferred, C_{Red} and C_{ox} are the initial concentrations, other terms have their usual meaning.

2. With a small potential perturbation ΔE and when the rate of charge transfer is limiting (12),

$$\Delta E = \Delta E_i \cdot \exp(-t/\tau_c) \qquad (1-3)_i$$

where

$$\tau_{o} = \operatorname{RTC}_{d1}/\operatorname{nFi}_{e} \tag{1-4},$$

where i, is the exchange current density for the charge transfer process.

3. With a large potential perturbation ΔE , and with a fast charge transfer reaction,

$$\Delta E = (2nF(D_{Red})^{1/2}C_{Red}/(\pi)^{1/2}C_{d1})t^{1/2} \qquad (1-5).$$

For $C_{d1} = 20 \ \mu F/cm^3$, $D_{Red} = 10^{-5} \ cm^3/sec$, and n = 2, ΔE is equal to 0.344 $t^{1/3}$, 0.0344 $t^{1/3}$, 0.00344 $t^{1/3}$ volts/(sec)^{1/3} for $C_{Red} = 10^{-5}$, 10^{-6} , 10^{-7} moles/liter, respectively (8,10).

The potential-time relaxation is a function of the double layer capacitance which is a function of potential. One of the major disadvantages of the original coulostatic method is that the variation of double layer capacitance with potential complicates the analysis of the desired parameters.

The coulostatic method reduces the effect of solution resistance because the current is zero when the electrode potential is measured. Therefore, the coulostatic method can be applied to most non-aqueous solutions and lower concentrations of supporting electrolyte can be used to

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reduce contaminations from the addition of the supporting electrolyte. In spite of the above advantages, which make the coulostatic polarization an attractive technique for analytical applications, only a few applications of this method have been reported (some examples can be found in References 11,13-16). This may be due to the lack of rapid and intelligent control system with which to produce the traditional varieties of polarizing potential or current waveforms through charge pulse injection.

Charge pulse injection, which is used in traditional coulostatic method to change the potential of a working electrode to a new value, can be used as well to maintain the electrode potential at a desired value as long as the size of the injected charge is small and the frequency of injection is sufficiently high.

The idea of controlling the electrode potential by charge injection has been used by Goldworth and Clem to develop the 'bipolar digipotentiogrator' (17). The system uses the charge injection technique to maintain a desired potential and simultaneously serves as a current-to-digital converter. The system injects small charge pulses of constant size to the working electrode as often as needed to keep its potential at the desired value. The design of the bipolar digipotentiogrator is based on dedicated hardware logic.

B. A Microcomputer Controlled Instrument:

A Programmable Coulostatic Polarizer (PCP)

In order to enhance the flexibility of the coulostatic method, a general purpose electroanalytical system based on a microcomputer-controlled coulostatic generator has been Houdakis (18). developed by Spyros This first microcomputer-controlled instrument has been improved substantially in this thesis work. The goals of the research work described here were to develop a flexible electrochemical instrument around the computer-controlled charge injection principle and to apply this instrument to electrochemical experiments which standard electrochemical instruments cannot easily perform.

Computers have been used heavily during the last decade in the development of modern automated instruments. The dedicated computer can operate the instrument, calibrate the instrument, collect data, and make decisions during the course of the experiment. Microprocessors with greatly improved performance have been introduced into the market in recent years. Some microprocessors have an instruction cycle as low as 0.8 μ s. Microprocessors, with the aid of supporting chips such as memory, input-output registers, buffers, communication chips, interrupt controllers, timing controllers, etc., can be used as an intelligent controller for virtually any instrument. The microcomputer used in this instrument is a single microcomputer segment of a

multi-microcomputer system that has been developed in our research group (19). These microcomputers are based on the 8085 microprocessor chip first manufactured by Intel Corp.

The block diagram of the instrument is shown in Figure 1-3. It consists of:

- 1. an electrochemical cell with working, reference, and counter electrodes (W, R, and C),
- a charge generator for the injection of charge to the electrode,
- 3. a voltage measurement system to monitor working electrode potential between charge injections,
- 4. a keyboard (KB) and a display (CRT) to allow operator communication,
- 5. a graphic controller to present the results of experiments in graphic form on the CRT,
- 6. a real time clock to control the timing of experiments, and
- 7. a floppy disk to store data.

The instrument is interfaced to a PDP 11/40 minicomputer to allow loading programs from its mass storage, and to use the minicomputer for faster data analysis and for hard copies of experimental results.

The instrument will be called the programmable coulostatic polarizer (PCP) since it is flexible through programming and can generate many types of coulostatic polarization waveforms. Several modes of operation of the



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Figure1-3. Block diagram of the programmable coulostatic polarizer (PCP)

PCP will be described in Chapter 2, examples of applications will be presented in Chapters 3 and 4, and some possible applications will be discussed in Chapter 5.

CHAPTER 2

THE DESIGN AND THE OPERATIONAL MODES OF THE PCP

A. Description of the Charge Generator and the Voltage Measurement System

Figure 2-1 shows a diagram of the charge generator and the voltage measurement system. The charge generator and the voltage measurement system are essentially the same those designed by Spyros Hourdakis (18) except that a few components have been changed; a separate power supply is used, and grounding circuits have been redesigned and constructed in order to reduce noise. A detailed description of the circuits can be found in Hourdakis' thesis (18). Some modifications to these circuits will be discussed in Chapter 6. The design principles and important characteristics of the components and circuits will be discussed in this chapter.

The charge generator consists of a 10-bit digitalto-analog coverter (DAC) and several capacitors, switches, and operational amplifiers. The microcomputer can control the DAC to produce a programmable voltage V. If the data for the DAC control is stored in the computer memory, the process of reading the data and controlling the DAC takes about 17 μ s. The DAC is of bipolar construction with a maximum output voltage of ± 2.5 V. It has a quantization





level of 4.88 mV. It is based on the AD7522 chip from Analog Devices which has maximum differential non-linearity of \pm 1/2 LSB and an overall non-linearity of less than 0.05% full scale range.

The inverter, which consists of operational amplifier A2, and resistors R1 and R2, converts the programmed voltage V to the voltage -V of equal magnitude, but with opposite polarity. Both voltages V and -V are available simultaneously in order to save the time of reprogramming the DAC if the opposite polarity of voltage is needed.

The microcomputer can control the closing of switches by which the capacitors C1 or C2 can be charged to -V or the capacitors C3 or C4 charged to +V. A capacitor which has a capacitance below 0.01 μ F, takes less than 13 μ s to reach 1/2 LSB (2.44 mV) for a step change of 2.5 V or less.

The switches are SIGNETICS SD5000 FET analog switches with on resistance of 30 Ω and off resistance of 10^{10} Ω. The leakage current is below 1 nA. The input capacitance is below 2.4 pF and the output capacitance is below 1.3 pF. Probably due to the coupling of the switch capacitance with the power sources (\pm 15V), a small amount of charge (about 20 pC) is always injected to the working electrode together with of the desired charge. This error is corrected by adjusting the baseline value of the DAC for a specific size of capacitor used. The adjustment can be performed automatically with the program BLVC.MAC which can be found in Appendix B. The operational amplifier A3 is a Datel AM 405-2 FET input, high-speed amplifier with high input impedance of 10^{13} Ω and a fast slew rate of 120 V/µs. The amplifier can supply a continuous current of \pm 10 mA and a peak current of \pm 50 mA.

The microcomputer can connect the capacitor which has been charged to the inverting input of operational amplifier A3 by closing the proper switch (e.g., switch SW2 for the The connection of the capacitor to the capacitor C1). amplifier makes the voltages different at the two inputs of the amplifier. When the operational amplifier A3 detects a voltage difference between its two inputs, it amplifies this difference by a very large gain which is usually between 10^4 and 10^5 . The voltage of the capacitor (one of C1-C4) usually exceeds ± 100 mV and the result of this amplification exceeds the power supply voltage (\pm 15V). In this situation, the output voltage limits at 1 or 2 V less than the power supply voltages. The change of output voltage of A3 creates a potential drop across the solution and charge flows from the output of A3 through the counter electrode, through the solution, through the working electrode to the capacitor in order to reduce the voltage difference between the inputs of the amplifier A3. The current continues until the voltages at both inputs of A3 A consequence of this process is that the are equal. capacitor (one of C1-C4) is discharged and the charge is

transfered to the working electrode. If the working electrode is an ideal polarizable electrode (an electrode which does not leak any charge across the electrode solution interface), the charge is accumulated on the electrode. If it is not, the accumulated charge is equal to the charge injected minus the charge consummed by the Faradaic process. A capacitor C5 is placed in the feedback loop of amplifier A3 to prevent oscillation. The capacitor does not slow down the response of the circuit significantly if its value is small (such as below 1000 pF). The recommended value for this capacitor is between 100 and 1000 pF which is usually much less than the value of the electrode double layer capacitance. When a charge pulse is injected, a very small portion (between 0.01 and 0.1 %) of the charge goes to the capacitor C5 if the electrode capacitance has a value of 1 µF. This error is so small that it can be neglected. Using this circuit, a charge can usually be injected to the working electrode within 50 µs for solutions with an electrolyte concentration above 0.01 M.

The voltage measurement system consists of a voltage follower A4, an amplifier A5 with a gain of 2, a sample and hold circuit, and a 12-bit analog-to-digital converter (ADC). The ADC is a DATEL ADC-HY12BC 12-bit successive approximation analog-to-digital converter with a conversion time of 8 μ s and an input range of \pm 5 V. The voltage of the reference electrode is amplified with a gain of 2 before it is fed in the input of the ADC. The resolution of the ADC is 2.44 mV which corresponds to 1.22 mV resolution in the electrode potential measurement. The purpose of using the amplifier with a gain of 2 and the low pass active filter is to reduce noise. The operator can regard the quantitization level of the voltage measurement system and of the electrode potential as 1.22 mV.

The microcomputer can command the ADC to convert the electrode potential to digital form. The microcomputer can then store the data, or compare the measured potential to the desired value and use this information to control the potential or make other types of decisions.

Several capacitors and associated switches can be used in the charge generator to enable the injection of both positive and negative polarities and different sizes of charge.

The charge generator and the voltage measurement system perform only two simple operations on the electrochemical cell: inject charge pulses to the working electrode and measure the potential of the working electrode. The use of a microcomputer as the control element of the operation of the PCP gives greater flexibility than it is found in conventional instruments. The operator has the ability, by programming, to determine how the operations of the charge generator and the voltage measurement system are to be used in order to perform an experiment, and he or she can program
the instrument to produce and apply any complex potential or current waveforms to the electrode. The flexibility of the PCP allows the operator to perform whichever electrochemical technique provides the best response for the investigated chemical system and also to perform some experiments which are difficult to perform with other instruments.

In addition to the flexibility of implementing a variety of polarization modes, the PCP maintains the characteristic advantages of coulostatic method, i.e., IR drop errors can be eliminated so that higher solution resistance causes fewer problems and double layer capacitance information is available to aid in separating double layer charging and Faradaic current. Several operational modes are discussed in this chapter.

B. Controlled Charge Mode

The operator can program the DAC to obtain a voltage V, and select a switch to charge a capacitor C, in order to produce a charge of value C·V coulombs which can then be transferred to the double layer of the working electrode. By choosing the capacitor value and the DAC voltage, the operator has the flexibility of generating a charge that can vary in size over several orders of magnitude. This allows the effect of the charge injection on the electrode potential to be varied from a fraction of a millivolt to several volts. The electrode potential is monitored both

before and after the charge injection.

The application of this operational mode to electrode capacitance measurement will be discussed in Chapter 3.

C. Controlled Current Mode

The equivalent of controlled current can be obtained either by controlling the quantity of charge per pulse to be injected to the working electrode and keeping charge injection rate constant, or by keeping the quantity of charge constant and controlling the charge injection rate. The current applied is equal to the charge per pulse times the frequency of pulse injection.

The application of this operational mode to constant current chronopotentiometry will be discussed in chapter 4.

D. Controlled Potential Mode

Controlled potential experiments can be achieved by comparing the measured potential to the desired potential at any specific time. If there is a difference, an appropriate amount of charge (e.g., the amount to make 1 mV potential perturbation) can be injected to the electrode in order to reduce the difference. This process can be repeated to within the resolution of the charge injection or potential measurement, whichever is larger. The desired potential can then programmed to vary with time. Virtually all kinds of controlled potential experiments (e.g., cyclic voltammetry,

pulse polarography, chronocoulometry, rotating disk voltammetry, etc.) can be performed in this way by using the PCP.

E. Open Circuit Mode

If no charge is injected on the electrode, the cell is in an open circuit condition. The measured potential is then obtained when there is no current passing through the solution. In this mode, the PCP can be used for the potentiometric measurements of a PH electrode or other ion selective electrodes.

F. Combinations of Modes

The PCP can switch from one mode of operation to another by changing from the program for one mode to that of the other. There are several advantages associated with this: First, the switching of the PCP from one mode of operation to another is fast. In most cases, the switching time is between 10 μ s and 1 ms. Second, the switching is done by software and involves no undesired charge added to the cell. Third, due to this flexibility, the PCP at times can perform some types of multi-mode experiments which cannot be carried out with other instruments. Fourth, the need for several differential instruments to perform various kinds of experiments can be avoided.

CHAPTER 3

EXAMPLE OF CONTROLLED CHARGE EXPERIMENT: Electrode capacitance measurements

A. Introduction

A capacitance measurement technique developed in this thesis work is discussed in this chapter. The technique is based on the charge injection method. Because charge pulses can be applied to the electrode automatically and very rapidly, capacitance measurements can be made very quickly. The potential is measured when there is no current passing through the solution so solution resistance causes fewer problems than with continuous current methods. Electrode capacitances can thus be obtained in solutions of quite dilute electrolyte. For example, capacitance measurements were obtained from a hanging mercury drop electrode (HMDE) in various concentrations of KCl solution down to 10^{-5} M. For concentrations of KC1 above 0.01 M, 37 equally spaced data points with 2 % relative standard deviation over a 1.45 V potential range were obtained within 30 seconds. The capacitances of a HMDE in dilute potassium chloride solutions were obtained within 150 seconds for a 10^{-4} M solution and within 30 minutes for a 10^{-5} M solution.

B. Background on Double Layer Capacitance Measurements

1. Qualitative Description of Electrical Double Layer

'Electrical double laver' is a term used to describe array of charged particles and oriented dipoles which the exist at any material interface. The modern qualitative electrical double picture of the laver at metal-electrolyte solution interface is based on the model proposed by D. C. Grahame (20-22). The model is modification of previous ones proposed by Gouy, Chapman, and Stern (23-26). The interface consists of several regions: (a) the metallic region, and, on the solution side, (b) an inner laver region of a few Å next to the metallic surface. and (c) an outer, or diffuse layer region which extends into the bulk solution a distance of several Å to a few hundred X.

The metallic surface may bear positive or negative electric charge resulting from an excess or deficiency of electrons. The electric charge may be imposed on the metal by an external source of electric charge or current, or it may be generated by a Faradaic reaction at the interface. The inner layer, which is on the solution side of the interface next to the metallic surface, contains solvent molecules and sometimes other neutral molecules. When the electrode potential is near or more positive than the potential of zero charge, in most electrolyte solutions the inner layer also contains a fractional monolayer of ions

(usually anions) which are said to be specifically adsorbed. The plane which is formed by the electric centers of these adsorbed ions is called the inner Helmholtz plane (IHP).

When the interaction between an ion and the metal **i s** not strong enough to desolvate the ion, the ion can not come as close to the metal as can those ions which 110 specifically adsorbed. The plane passing through the electrical centers of these solvated ions is called the outer Helmholtz plane (OHP). The principal interaction between the metal and the solvated ions is the ordinary long-range coulombic force and this is the basis of the Gouy-Chapman theory (23-25). The interaction of these ions with the metal is essentially independent of their chemical properties but dependent on their ionic charges and radius. The solvated ions near the electrode are said to be nonspecifically adsorbed. Due to the thermal motions of these ions, not all of them are located at the OHP, instead, they are contained in a three dimensional region, the diffuse layer, which extends from OHP into the bulk of the solution. The diffuse layer is very thin. For example, 99.99 % of excess charges in the diffuse layer for a uni-valent electrolyte at a concentration of 10^{-1} M is within $8.9 \cdot 10^{-7}$ cm, and at 10^{-5} M is within $8.9 \cdot 10^{-5}$ cm from the surface of the metallic electrode.

The metallic surface may bear a net electric charge and the solution side may have an excess of charged particles or

a net orientation of dipoles. The electrode interface looks like a capacitor and has the property of capacitance under equilibrium conditions. The differential capacitance of an electrode at constant chemical composition is defined as $(\partial q^m / \partial E)_{\mu}$, where q^m is the charge density on the metallic electrode, and E is the electrode potential. This term is very similar to (Q/V), the capacitance for an ideal capacitor.

2. Usefulness of Double Layer Studies

The measured fundamental parameters of electrode kinetics may be strongly dependent on the structure of the double layer. The rate constant k for a given electrode reaction might be a function of supporting electrolyte or its concentration. When there is no apparent reaction involving the electroactive species of interest (e.g. complexation or ion-pairing), a non-linear plot of the logarithm of the rate constant k versus potential can be observed.

Nost of these effects can be understood and interpreted in terms of the effect of electrolyte on the potential profile in the double layer region. In addition to the means of controlling electrode potential, double layer studies will provide chemists with a means to affect an electrochemical reaction through the variation in electrolyte composition.

In trace analysis, adsorption of electroactive species

sought may become a predominant factor for some For electroanalytical techniques. erample, in chronopotentiometry, adsorption of an electroactive constituent can give rise to an increased transition time. Failure to take account of adsorption can lead to serious errors. If the adsorption is properly interpreted, the sensitivity of an electroanalytical technique may be enhanced.

The double layer charging current, which is a non-Faradaic current required by the electrode during a change of electrode potential or area, is involved in all controlled potential techniques. The measurement of the double layer capacitance can help one to be aware of the relative magnitudes of the charging and Faradaic currents in an experiment. Moreover, a knowledge of the double layer capacitance may be used for the compensation of the double layer charging as will be demonstrated in chapter 4.

3. Nethods for the Neasurement of Double Layer

Capacitance

If the phenomena predicted by a theory do not agree with the experimental results, the theory is considered deficient. Double layer capacitance measurements are generally used in electrochemistry to test how good a theory on double layer structure is. A number of methods for the measurement of double layer capacitance have been developed and the more important ones will be discussed.

One type of capacitance measurement is performed by taking the second derivative of a curve of surface tension versus electrode potential. Let us consider the situation of a liquid mercury electrode in an electrolyte solution. The interfacial tension γ of a mercury-solution interface varies with the electrode potential E. If γ is plotted vs. E, the result is known as an electrocapillary curve. electrocapillary curves may be described by the **A11** electrocapillary equation developed by Grahame and Whitney (27), which is a differential equation relating the interfacial tension to the temperature, pressure, electrode potential, and chemical potentials of the components. The starting point of the derivation of the electrocapillary equation is the Gibbs adsorption equation. Some important relationships have been derived from the electrocapillary equation, e.g.:

$$q^{m} = (\partial \gamma / \partial E)_{\mu}$$
 (3-1)

$$C_{d1} = (\partial^2 \gamma / \partial E^2)_{\mu}$$
 and (3-2)

$$\gamma = \gamma_{pzc} - \iint_{pzc}^{E} C_{d1} \cdot dE^{2} \qquad (3-3)$$

where q^m is the charge density on the mercury electrode, C_{d1} is the double layer capacitance, and pzc represents the potential of zero charge on the electrode.

The capacitance of an electrode can be obtained by measuring the surface tension γ of the electrode, and then taking the second derivative of the electrocapillary curve, $(\partial^2 \gamma / \partial E^2)_{\mu}$. The limitation of this method is that it can usually be applied to liquid electrodes only.

Capacitance measurements are commonly performed with an AC bridge. Bridge measurements provide a direct way to compare unknown impedances with known standard impedances. The impedance may be supplied by a pure resistor, capacitor, or inductor, or may be supplied by a circuit represented by series or parallel combination of resistors, some capacitors, and inductors. The major advantage of this method is that it allows one to very accurately obtain electrode capacitance and kinetic parameters at high concentrations of electrolyte solution (e.g. above 10^{-5} M). This method has several disadvantages: First, it is tedious to adjust the values of resistors and capacitors in order to match the unknown impedance of the cell. Second. it requires significant skill to perform the experiment. Third, it can require more than 30 minutes to take data for the entire potential range (usually between 1 V and 2 V) and the electrode (especially for a solid one) may be more subject to the adsorption of organic impurities if it is left in the solution so long. Fourth, it cannot generally be applied to the determination of double layer capacitance with media of low conductivity or dilute electrolyte

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solutions (e.g. below 10^{-3} M for strong electrolyte).

Most commercial instruments presently available use a lock-in amplifier technique to measure electrode capacitance. The capacitance measurement can usually be completed within a few minutes. But again, these instruments are not well suited for very dilute electrolyte solutions.

L. Ramaley and co-worker (28) developed two methods of capacitance measurement. One method involved the application of a rectangular current waveform to the electrode. The capacitance C_{d1} was obtained by measuring the slope of the electrode potential vs. time curve:

$$C_{d1} = i \cdot dt / dE \qquad (3-4),$$

where <u>i</u> is the current density applied to the electrode, and dE/dt is the slope of the potential time curve. Because a constant current is applied to the electrode and the solution resistance does not change during the current pulse, the IR drop is constant and it does not affect the value of dE/dt. This can be very important in high resistant solutions. The other method involved the use of small sinusoidally varying potential of constant amplitude. With an oscilloscope as a fast detection system, they measured electrode capacitance with a sweep speed of a volt per second or more for solutions with high conductivity.

The speed and ease of measurement is importment for the measurement of capacitance of silver in perchlorate solution (29).

P. Delahay and co-workers (30) used a coulostatic charging method to change the electrode potential slightly (5-15 mV) and measure the potential change due to a known charge. The charge Q on a small capacitor (500 pF) which had been charged to at least 1 V was transfered to the working electrode by a differential electrometer-amplifier. Measurements were read out on an oscilloscope and compared with those obtained with a dummy cell. Because of the temporary overload of the amplifier, they took readings 50 to 100 ms after the pulse and extrapolated the results back to time zero when necessary. The capacitance C_{d1} of the electrode is Q/V_a , where Q is the charge transferred to the electrode and V, is the potential change resulting from the charge addition. The major advantage of this method is that double layer capacitance can be measured in media of low conductivity.

C. Description and Test of the Technique:

The technique for capacitance measurement developed in this research work is based on the ideas of P. Delahay and his co-workers (30), except that we have used a microcomputer for automatic charge injection and data collection. These allow the capacitance to be measured very

quickly. Because a fast response amplifier (AM 405-2) was used, the overloading of the amplifier is not the limiting factor in the delay time required for potential measurement. The potential perturbation can be measured at a shorter time after the charge pulse using this technique than the method developed by P. Delahay and co-workers. For example, with a concentration of KC1 of 10^{-3} M, the delay time can be as short as 6.4 ms. (The shortness of delay time is limited by the resistance of the electrolyte, not by the amplifier overloading.) Multiple measurements also enhance the precision of the results.

Figures 3-1 to 3-3 show a flow chart of the program which was used for the capacitance measurement. The operator sets up the parameters such as initial potential E_{init} , final potential E_{final} , potential perturbation ΔE for capacitance measurement, potential interval E_{inc} between two adjacent data points, desired minimum number of repetitive measurements N_d for each potential, delay time t_d between charge injection and voltage measurement, etc., before starting the data acquisition.

After the operator starts the program, the PCP measures the electrode potential and compares it to the desired initial value E_{init}. If there is any difference in the potential values, the PCP injects charge pulses of proper polarity in order to bring the electrode potential to the initial value. After the initial potential is reached, the



E _{init} :	initial potential
Efinali	final potential
∆E _{flag} :	flag for calculation
	of charge size in
	order to start at
	proper potential
E:	Electrode potential
Ed:	desired potential for
	taking data
ΔE:	potential perturbation
∆E _{inc} :	potential
	increment to next
	data point
۵E _d :	desired potential
	perturbation
ΔE _{min} =	ΔE _d -1.22mV
∆E _{max} =	ΔE _d +1.22mV
t _d :	desired time
	delay between
	charge injection
	and voltage
	measurement
N(E):	number of data
	points at potential
	E
Nd:	desired minimum
	number of data
	points
Q:	charge to be injected
Q _{inc} :	charge increment

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Figure 3-1. The first part of the flow chart of the program for the capacitance measurement





The second part of the flow chart of the program for the capacitance measurement





The third part of the flow chart of the program for the capacitance measurement

PCP keeps the electrode potential at this value by adding charge as needed until the operator commands the PCP to execute data acquisition. An amount of charge previously specified by the operator is injected to the electrode and the potential E is measured after some delay time t_d. The potential perturbation ΔE due to the charge injection is calculated and compared to the desired value ΔE_d . The computer automatically calculates the amount of charge Q to be injected on the electrode in order to make $\Delta E = \Delta E_d$ -3.66 mV. For a typical measurement, $\Delta B_d = 9.77$ mV which corresponds to 8 times the least significant bit (LSB). The quantization level of the voltage measurement system is 1.22 mV. A potential change of 9.77 mV is selected because it is not large and can be used for differential capacitance measurements. The value $\Delta E_d = 3.66$ mV is chosen for two reasons: The first is because the quantity of charge Q to be injected will be incremented and the number of charge injections which will make ΔB within $\Delta B_d \pm 1.22$ mV will be counted. The value $\Delta E_d = 3.66$ mV is not too far from the value ΔE_d - 1.22 mV and starting the charge incrementation from the amount of charge which will achieve this potential perturbation can save experimental time. The second reason is that the value ΔE_d - 3.66 mV differs from the value ΔE_d -1.22 mV by 2.44 mV, which corresponds to 2 LSB. This difference can provide some noise rejection.

The size of charge Q is incremented automatically after

 $\Delta E = \Delta E_d - 3.66$ mV is reached, the potentials are measured before and after charge injection, and the potential perturbation is calculated for each injected charge pulse. If ΔE is within $\Delta E_d \pm 1.22$ mV, the amount of injected charge is accumulated and the counter is incremented by 1. As soon as $\Delta E \ge \Delta E_A$ + 3.66 mV, the computer will read N(E), the content of the counter for potential E. The computer compares N(B) to N_d , the desired minimum number of data points previously specified by the operator. If $N(E) \ge N_d$, the control will go to the next potential and perform charge injections and data collection until the final potential is reached. If $N(E) < N_d$, more data points will be taken before going to the next potential. The results are plotted on a CRT display immediately after the experiment is completed to allow the operator to observe the experimental results.

According to the manufacturer, the calibration of the analog-to-digital converter (ADC) is accurate to $\pm 1/2$ least significant bit (LSB) which corresponds to ± 0.61 mV of electrode potential in this instrument design. Although it is possible to reduce this uncertainty by using an ADC with higher resolution, the cost will be higher. The voltage differences ΔV_1 and ΔV_2 shown on Figure 3-4 may not be equal even if they give the same results in the conversion to the digital domain. The consequence of this is that different amounts of charge Q_a and Q_b are required to make the voltage



perturbation ΔV_1 and ΔV_2 respectively although the ADC reports them as the same. This can introduce error in the calculation of capacitance if the results are interpreted as $Q_a/(\Delta V_{idea1}\cdot A)$ and $Q_b/(\Delta V_{idea1}\cdot A)$, where A is the area of the electrode and ΔV_{idea1} is the ideal voltage perturbation corresponding to the obtained digital result from ADC.

Figure 3-5 shows the capacitance curve obtained for a 0.995 μ F capacitor using the equation:

$$C = Q_1 / (N_1 \cdot \Delta V_{ideal})$$
 (3-5),

where C is the calculated capacitance, Q_1 is the accumulated charge. The number of charge injections is N_1 , and $\Delta V_{ideal}=9.77$ mV. A dummy cell shown in Figure 3-6 with the capacitor C_{dm} in the feedback loop was used and more than 1000 data points were taken for each voltage. The capacitor C_{dm} is mylar and the capacitance is constant throughout the voltage range used in all of the following experiments.

A method to reduce this calibration error was needed in order to improve the results. Equation 3-6 shows the double layer capacitance $C_{d1}(E)$ at potential E in terms of the accumulated charge injected to the electrode Q_{e1} , the number of charge injections N_s , the potential perturbation ΔE , and the electrode area A. A dummy cell with capacitance C_{dm} is used to perform a similar experiment. Equation 3-7 relates the accumulated charge Q_{dm} , the number of charge injections DUMMY CELL, $C_{dm} = 0.995 \text{ uF}$







Figure 3-6. Dummy cell configuration for testing the capacitance measurement technique

 N_1 , and the voltage perturbation ΔV , which is equal to ΔE at the same voltage or potential. If Equation 3-6 is devided by Equation 3-7, Equation 3-8 is obtained.

$$Q_{e1}/(N_2 \cdot \Delta E \cdot A) = C_{d1}$$
(3-6)

$$Q_{dm}/(N_1 \cdot \Delta V) = C_{dm}$$
(3-7)

$$Q_{e1} \cdot N_1 \cdot C_{dm} / (N_2 \cdot A \cdot Q_{dm}) = C_{d1}$$
 (3-8)

By doing this, the error or uncertainty in ΔE or ΔV , in the calibration of ADC is cancelled out. If C_{dm} is known, C_{d1} can be calculated accurately without being affected by the calibration error.

Two identical experiments were performed with the same dummy capacitor. The capacitance of the capacitor $C_{dm2}(V)$ at voltage V is calculated by assuming that the capacitance C_{dm1} of the capacitor is constant (0.995 µF) and by using Equation 3-9 which is similar to Equation 3-8.

$$C_{dm2}(V) = Q_2 \cdot N_1 \cdot C_{dm1} / (N_2 \cdot Q_1)$$
 (3-9),

where Q_1 and Q_3 are accumulated charges for individual experiments, N_1 and N_3 are the number of charge injections for individual experiments, and $C_{dm1} = 0.995 \ \mu F$.

Figure 3-7 shows the results obtained by this method.

DUMMY CELL, $C_{dm} = 0.995 \text{ uF}$





In each experiment, the desired minimum number of charge injections, N_d, is 1000. The method clearly removes the effect of calibration error in ADC.

Quantization error is encountered in all analog-to-digital conversions. A common method to reduce the quantization error is to add to the signal white random noise whose amplitude is comparable to the quantization level of the ADC and then average repetitive measurements. The standard deviation of the average from a number of measurements can be less than the quantization level of the ADC.

Although AAE, the difference in potential perturbations of two adjacent charge injections, is usually smaller than the quantization level of the voltage measurement system (in this instrument design, the quantization level is 1.22 mV), the quantization error is less for two reasons: First, the amount of charge injected to the electrode is incremented and averaged through the potential difference window ΔE_d $\pm 1.22 \text{ mV}$ several times until the number of data for each potential is greater than N_d. Second, the potential before charge injection to make the perturbation ΔE has a small random variation.

According to Kelly and Horlick (31), the standard deviation of quantization noise is $v_1/12^{0.5}$, where v_1 is the magnitude of the quantization level. In this case, $v_1 = 1.22$ mV. Since voltage differences are taken, the standard

deviation should be $1.22 \cdot 2^{0.5}/12^{0.5}$ mV. If N is the number of data points to be averaged, the estimated standard deviation is

$$1.22 \cdot 2^{0.5} / ((N-1)^{0.5} \cdot 12^{0.5}) \text{ mV}$$
 (3-10).

For a typical application where $\Delta E = 9.77 \text{ mV}$ and N = 20, the estimated % relative standard deviation (% RSD) is

$$(1,22\cdot(2)\cdot 5/((20-1)\cdot 5\cdot 9.77\cdot(12)\cdot 5)\cdot 100\% = 1.17\%$$
 (3-11).

For $\Delta E = 9.77$ mV and N = 100, it is

$$(1.22 \cdot (2) \cdot 5/((100-1) \cdot 5 \cdot 9.77 \cdot (12) \cdot 5) \cdot 100\% = .512\%$$
 (3-12).

For $\Delta E = 9.77$ mV and N = 1000, it is

$$(1.22 \cdot (2) \cdot 5/((1000-1) \cdot 5 \cdot 9.77 \cdot (12) \cdot 5) \cdot 100\% = .161\%$$
 (3-13).

By choosing the value of N, the desired precision of the measurement can be obtained. Figures 3-8 and 3-9 illustrate the precision of this technique. The experimental results were calculated using Equation 3-9. Figure 3-8 shows the results obtained with $N_{d1} = 100$ and N_{d2} = 1000, where N_{d1} is the desired minimum number of charge injections for the first scan and N_{d2} is the desired minimum







Capacitance ratio obtained with N_{d1} =100, N_{d2} =1000, and t_d =0.8 ms





Figure 3-9.

Capacitance ratio obtained with N_{d1} =1000, N_{d2} =1000, and t_d =0.8 ms

number of charge injections for the second scan. The average relative standard deviation was found to be 0.54 %. Figure 3-9 shows the results obtained with $N_{d1} = 1000$ and $N_{d2} = 1000$. The average relative standard deviation was found to be 0.19 %. The capacitance measurement results obtained in the next section are all performed using $N_{d2} = 1000$ for the dummy cell measurements.

D. Double Layer Capacitance Measurements for Mercury Electrode in KC1 Solutions

When the capacitance measurement technique was tested with a mercury electrode in KC1 solutions, a hanging mercury drop electrode (HMDE) was used as a working electrode and the capillary of the HMDE was siliconized on a daily basis to prevent capillary action of the test solution. A micrometer drive HMDE was used. The weight of mercury with the turning of 200 divisions of the micrometer drive was measured (0.2112 grams). The weight (8.45 mg) of each mercury drop with the turning of 8 divisions was calculated by dividing the weight (0.2112 g) by 25 (assuming that the micrometer drive is uniform). The area (0.0352 cm^2) of the mercury drop was calculated by assuming that it has a spherical shape. The reproducibility of the area was determined by measuring the amount of charge required to make a potential perturbation of 400 mV (for details, see the integral capacitive charge measurement in Chapter 4) in

0.1 M KCl solution for 20 different mercury drops under the same experimental condition. The relative standard deviation of the area was found to be 1.1%.

A saturated calonel electrode (SCE) was used as the reference electrode. A 1.0 cm^2 platinum gauze was used as a counter electrode. Potassium chloride solutions were used because KC1 is the most commonly used supporting electrolyte for analytical work. By using KCl solutions, the leakage of a trace amount of KC1 from the SCE reference electrode causes fewer problems. Sodium fluoride solutions 818 generally used to test Gouy-Chapman double layer theory (23-25) because it is believed that sodium fluoride does not adsorb on the mercury surface. However, for experimental simplicity, KCl solutions were used to test the performance of the PCP for capacitance measurements. Reagent grade potassium chloride had been recrystallized before it was Solutions were prepared with de-ionized water which used. had been passed through an ion-exchange column manufactured by Millipore. The solution in the cell was de-serated with nitrogen gas which had been passed through a vanadous chloride solution to remove trace oxygen. All experiments were performed at room temperature between 20.5 and 21.5 °C except when noted otherwise.

Figure 3-10 shows the capacitance curves obtained with 1.0 M KC1 solution. These curves were obtained with N_{d1} = 100, N_{d2} = 1000, and t_d = 0.4, 0.8, 1.6, 3.2 ms. The

1.0 M KCI



Figure 3-10.

Capacitance curves of HMDE in 1.0 M KCI solution obtained with N_{d1} =100, N_{d2} =1000, and t_d = 0.4, 0.8, 1.6, 3.2 ms with the higher capacitances corresponding to the longer times

capacitance values increased with increasing values of t_d (the delay time between charge injection and voltage measurement) N_{d1} is the minimum number of charge injections for the electrochemical cell, and N_{d2} is the minimum number of charge injections for the dummy cell.

Figure 3-11 shows the capacitance curves obtained with 0.1 M KCl solution. These curves were obtained with N_{d1} = 100, N_{d2} = 1000, and t_d = 0.2, 0.4, 0.8, 1.6, 3.2, 6.4 ms.

Figure 3-12 shows the capacitance curves obtained with 0.01 M KC1 solution. These curves were obtained with N_{d1} = 100, N_{d2} = 1000, and t_d = 0.2, 0.4, 0.8, 1.6, 3.2, 6.4, 12.8

The capacitance approaches a fairly constant value after some delay time. This is because it takes some time for the charge to be injected to the electrode and the charge needs to be distributed evenly on the electrode before an accurate reading can be taken. For lower concentrations of KC1 solution, it takes longer to get a constant capacitance value (equal charge distribution) because the solution is less conductive.

Figure 3-13 shows the capacitance curves obtained with 10^{-3} M KCl solution. These curves are obtained with N_{d1} = 100, N_{d2} = 1000, and t_d = 0.4, 0.8, 1.6, 3.2, 6.4, 12.8, 25.6 ms.

In order to speed up the cell response, the first attempt was to decrease the solution resistance between the 0.1 M KCI



Figure 3-11.

Capacitance curves of HMDE in 0.1 M KCI solution obtained with $N_{d1}=100$, $N_{d2}=1000$, and $t_d = 0.2$, 0.4, 0.8, 1.6, 3.2, 6.4 ms with the higher capacitances corresponding to the longer times

0.01 M KCI



Figure 3-12.

Capacitance curves of HMDE in 0.01 M KCI solution obtained with $N_{d1}=100$, $N_{d2}=1000$, and $t_d = 0.2$, 0.4, 0.8, 1.6, 3.2, 6.4, 12.8 ms with the higher capacitances corresponding to the longer times



Figure 3-13.

Capacitance curves of HMDE in 10^{-3} M KCl solution obtained with N_{d1}=100, N_{d2}=1000, and $t_d = 0.4$, 0.8, 1.6, 3.2, 6.4, 12.8, 25.6 ms with the higher capacitances corresponding to the longer times

working electrode and the counter electrode by putting the counter electrode as close to the HMDE working electrode as possible. The shortest distance between the counter electrode and the HMDE was approximately 0.15 cm and the capacitance curves for KC1 solutions mentioned previously in this section were all obtained by this cell geometry. However, this cell geometry was actually found to slow down the cell response because of the lack of uniformity of the current density.

In order to obtain a more uniform current density, a cell with the counter electrode about 1.0 cm away from the HNDE and with the SCE reference electrode on the other side of the HNDE was used for the following experiments of capacitance measurement. A salt bridge filled with the same concentration of test solution was placed between the SCE and the test solution in order to reduce the leakage of KC1 from the SCE to the test solution. This salt bridge was used for all KC1 concentrations below 10⁻³ M.

Figure 3-14 shows the capacitance curves obtained with 10^{-3} M KC1 solution and with the improved cell geometry. These curves were obtained with $N_{d1} = 100$, $N_{d2} = 1000$, and $t_d = 0.2$, 0.3, 0.4, 0.8, 1.6, 3.2 ms. The capacitance curve obtained with $t_d = 6.4$ ms almost coincides with that obtained with $t_d = 3.2$ ms. For clarity, it has been omitted from the figure. If we compare these results to those shown on Figure 3-13, clearly, we find that the new cell geometry


Figure 3-14.

Capacitance curves of 10^{-3} M KCI solution obtained with improved cell geometry These curves are obtained with N_{d1}=100, N_{d2}=1000, and t_d = 0.2, 0.3, 0.4, 0.8, 1.6, 3.2 ms with higher capacitances corresponding to the longer times improves the cell response significantly.

Figure 3-15 shows the capacitance curves obtained with 10^{-4} M KCl solution. These curves were obtained with $N_{d1} = 20$, $N_{d2} = 1000$, and $t_d = 0.8$, 1.6, 3.2, 6.4, 12.8, 25.6 ms. The number 20 instead of 100 was used for N_{d1} to reduce experimental time although this resulted in some sacrifice in precision. A typical time spent for data collection at 10^{-4} M with $t_d = 12.8$ ms is 150 sec. The estimated precision due to quantization error for $N_{d1} = 20$ is 1.17 % RSD according to Equation 3-11.

Figure 3-16 shows the capacitance curves obtained with 10^{-5} M KCl solution. These curves were obtained with N_{d1} = 20, N_{d2} = 1000, and t_d = 12.8, 25.6, 51.2, 102.4 ms.

Figure 3-17 shows the capacitance curves for a range of KC1 solution concentrations. Each curve was obtained from the capacitance values which approached a relatively constant value at the longer delay times. The delay times t_d were 0.4, 1.6, 6.4 ms for 1.0, 0.1, 0.01 M KC1 solutions respectively, and the t_d 's were 6.4, 12.8, 102.4, ms for 10^{-3} , 10^{-4} , 10^{-5} M KC1 solutions with improved cell geometry. For a 10^{-5} M KC1 solution, the capacitance did not quite reach a constant value, especially at the more positive potentials. This makes the determination of the capacitance value somewhat difficult.

The major sources of error will be discussed. The % RSD of the area of HMDE is 1.1 %. The estimated



Figure 3-15.

Capacitance curves of HMDE in 10^{-4} M KCI solution obtained with N_{d1}=20, N_{d2}=1000, and t_d = 0.8, 1.6, 3.2, 6.4, 12.8, 25.6 ms with the higher capacitances corresponding to the longer times



Figure 3-16.

Capacitance curves of HMDE in 10^{-5} M KCI solution obtained with N_{d1}=20, N_{d2}=1000, andt_d = 12.8, 25.6, 51.2, 102.4 ms with the higher capacitances corresponding to the longer times

KCI SOLUTIONS





quantization error for $N_{d1} = 20$ is 1.17 % RSD and for $N_{d1} = 100$ is 0.51 % RSD according to Equations 3-11 and 3-12. The impurities in the KC1 solutions may be an important consideration when KC1 concentration is high, e.g. above 10^{-3} M; however, no significant drift of capacitance curves were observed after the capacitance reached a constant value. The leakage current (1.9 nA) from the analog switches is the main source of error when the concentration of KC1 is low (below 10^{-3} M) and t_d is large. The reduction of oxygen or reduction of other electroactive species is the second; fortunately, it tends to cancel out the error of current leakage. The measurement error of the double layer capacitance, ΔC_{d1} , due to the current leakage can be estimated by the equation

$$\Delta C_{d1} = (1.9 \text{ nA}) \cdot t_{d} / (A \cdot (0.00977 \text{ V})) \qquad (3-14),$$

where A is the electrode area 0.0352 cm^3 . Table 3-1 lists the expected error due to the leakage of current of the switch.

		_
t _d (ms)	Brror (µF/cm²)	
1.6	0.0088	
3.2	0.018	
6.4	0.035	
12.8	0.071	
25.6	0.14	
51.2	0.28	
102.4	0.57	

Table 3-1 Estimated error due to the switch current leakage for the capacitance measurement

This error estimation suggests that the capacitance can be obtained accurately for concentrations of KCl solutions above 10^{-4} M if the leakage of current is the only concern. The largest relative error for 10^{-4} M KCl solution due to the current leakage is

$$(0.071 \ \mu F/cm^{2})/(2.95 \ \mu F/cm^{2}) \cdot 100\% = 2.4\%$$
 (3-15),

where 2.95 μ F/cm³ is the minimum capacitance of 10⁻⁴ M KC1 solution within the potential window tested. The estimated % RSD for individual potential due to the quantization error of ADC is 0.51 % for N_{d1} = 100, and 1.17 % for N_{d2} = 20.

For 10^{-5} N KCl solution, the error in the capacitance measurement due to the switch leakage current can be as high as 0.57 μ F/cm³. Some uncertainty in the capacitance value is also observed at potentials more positive than -0.15 V vs. SCE.

E. Discussion

Figure 3-18 shows the results obtained with this technique and the results obtained by D. C. Grahame (32) for 0.1 N KCl solution at 25 °C. These results agree with each other within experimental error.

The advantages of the technique are: First, the tedious manual work in adjusting resistors and capacitors to match up the cell impedance can be avoided. Second, the computer can take data automatically and human error can be avoided. Third, no special skill is required to take data. Fourth, fast data collection allows better results to be obtained. It is known that the capacitance of an electrode usually changes with time due to the adsorption of impurities. For a solid electrode, it is more difficult to renew the electrode surface than for a mercury electrode. Faster data collection will be subject to less error because the electrode has less time to adsorb impurities. For concentrations of KCl solutions above 10^{-3} M, data can be obtained within 30 seconds for $N_{d1} = 100$, and within 8 seconds for $N_{d1} = 20$. For 10^{-4} M, a typical experiment time is 150 seconds for $N_{d1} = 20$. Although measurements at a stationary mercury electrode are probably subject to some

0.1 M KCI



Figure 3-18.

Capacitances of mercury electrode in 0.1 M KCl solution this work; +++. Grahame's data poisoning, in this research work no significant time dependence effects on capacitance measurements were observed.

For some solid electrodes, measurement speed may be more important than accuracy or precision of measurement. (Errors of several percent of error may be tolerable.) With some sacrifice in precision (using $N_{d1} = 20$, the expected precision due to the quantization of ADC is 1.17 % RSD), a capacitance profile (37 data points) can be obtained within 8 seconds for 1 M KC1 solution.

Based on the charge injection principle, further enhancement of the speed of capacitance measurement can be achieved by using a higher resolution ADC which would require less data averaging.

The major disadvantage of this technique is that it is not suitable for capacitance measurements with electroactive species in the solution.

The cell geometry can probably be improved further to provide more uniform current density and faster response by using a fine-tipped capillary (33) with a cylindrical counter electrode surrounding the capillary.

CHAPTER 4

EXAMPLE OF CONTROLLED CURRENT EXPERIMENT: CONSTANT CURRENT CHRONOPOTENTIONETRY WITH CHARGING CURRENT COMPENSATION

A. Introduction

When a constant current is applied to an electrode, part of the current goes to charge the double layer capacitance. The desired constant Faradaic current is no longer constant because of the charging current. The chronopotentiogram (potential vs. time curve) obtained is distorted by the charging current. As will be noted later in this chapter, the transition time (the time interval between two sharp potential changes) is longer than that predicted by the theory.

A method has been developed in this thesis work to compensate the charging current in order to make the Faradaic current constant using the charge injection method. Charge pulses are injected at a constant rate. The portion of the charge which has been consumed by Faradaic process from the beginning of the experiment to any specific injection time has been calculated by subtracting the charge which has gone to charge the double layer capacitor from the total charge injected. If there is any deviation between the desired Faradaic charge and actually consumed Faradaic

charge, a mechanism to minimize this deviation has been developed and used to make the Faradaic current constant. The mechanism of charging current compensation will be discussed in section C.

A great improvement of the correspondance between the measured transition times with the transition times predicted by the theory for a series of concentrations of T1C1 in 0.1 M KC1 solution is demonstrated in Section D. In-situ compensation of charging current allows the detection limit for cadmium to be as low as $2.5 \cdot 10^{-7}$ M.

B. Historical Background

If we apply a controlled current to an electrochemical cell and plot the potential as a function of time, we obtain a so called chronopotentiogram. As the name of chronopotentiometry infers, the potential is monitored as a function of time.

The most commonly used chronopotentiometric technique is to apply a constant current to an electrochemical cell. Let us suppose that we have a solution which contains sufficient supporting electrolyte and an electroactive species in the oxidized form Ox. The species Ox can undergo the reaction, $Ox + ne^- \Rightarrow \text{Red}$, by supplying electrons to the electrode. When a constant current \underline{i} is applied to the electrode, the species Ox is reduced at a constant rate. The potential of the electrode moves rapidly to some value

which is a characteristic of the redox couple. The potential varies with time as the Ox/Red concentration ratio changes at the electrode surface as a result of the conversion of Ox to Red. After the concentration of Ox at the electrode surface drops to zero, the flux of Ox to the electrode is no longer sufficient to bear the impressed current and the electrode potential changes very rapidly until the reduction of another electroactive species (when there is such a species which is reducible at more negative potential) or the decomposition of the solvent occurs. The time interval between these two sharp changes of the potential is called the transition time, τ . The transition time τ is related to a number of variables including the Ox concentration by the Sand equation (34):

$$i\tau^{1/2} = \pi^{1/2} nFAD_{0x}^{1/2}C_{0x}/2$$
 (4-1)

where \underline{i} is the applied constant current, \underline{n} is the number of electrons involved in the electrochemical reaction, \underline{F} is the Faraday, \underline{A} is the area of the working electrode, D_{OX} is the diffusion coefficient of Ox, and C_{OX} is the bulk concentration of Ox.

The measured τ can be used to determine the number of electrons involved in the reaction, or the diffusion coefficient D_{OI} , or the concentration C_{OI} . The transition time does not depend on the reversibility of the

electrochemical reaction since a constant current is forced through the cell to cause a constant rate of reaction. The independence of the transition time on reversibility is an advantage in analytical applications, especially for reactions which are not very reversible. The potential of the electrode during the transition, on the other hand, depends on the reversibility of the reaction. Therefore, chronopotentiometry can be used for studying the kinetics of electrochemical reactions. For a reversible reaction, the relationship between the electrode potential E and the time t (35) is:

$$E = E_{\tau/4} + (RT/nF) \cdot \ln((\tau^{1/3} - t^{1/3})/t^{1/3}) \qquad (4-2),$$

where $E_{\tau/4}$, the potential at the time $\tau/4$, is:

$$E_{\tau/4} = E_{f}^{\bullet} - (RT/2nF)(D_{ox}/D_{Red})$$
 (4-3),

where E_f^{\bullet} is the formal potential of the redox couple, and D_{Red} is the diffusion coefficient of Red.

For a totally irreversible reaction, the relationship between the potential E and the time t (36) is:

$$E = E_{f}^{\bullet} + (RT/an_{a}F)ln(nFAC_{ox}k^{\bullet}/i)$$

+
$$(RT/an_F)ln(1-(t/\tau)^{1/2})$$
 (4-4),

where a is the transfer coefficient, n_a is the number of electrons involved in the rate determining step, k° is the standard heterogeneous rate constant. The electrode potential shifts negatively with increasing current.

Chronopotentiometry can be used for the measurement of adsorption and surface film formation (37). By impressing high current to the electrode, the electrons consumed by the diffusion of Ox to the electrode surface can be minimized, Thus,

$$\lim_{i \to e} i\tau = nFPA \qquad (4-5),$$

where P is the surface excess, in moles/cm², of the adsorbed material.

If a reverse current is applied to the electrode after some experimental time t_1 , some of the Red formed during the forward period is oxidized to Ox. The potential changes rapidly when the concentration of Red at the electrode surface drops to zero. The separation of the forward and reverse waves on the potential axis can be related to the reversibility of the electrochemical reaction.

If both the forward and reverse processes are diffusion

controlled, if both currents have the same magnitude, and if the reaction has no chemical complications, the transition time for the reverse process, τ_1 , is related to the time t_1 , the time of applying the reverse current, by (38)

$$\tau_1 = t_1/3 \ (t_1 \leq \tau)$$
 (4-6).

If the Red undergoes some chemical reaction to form products P_{Red} which do not react in the same manner as Red does, the effect will be evident in the resulting chronopotentiogram. For example, if P_{Red} does not oxidize within the range of potentials in the experiment, the transition time τ_1 will be shorter than $t_1/3$. The chronopotentiometric potential/time relationships for a number of common electrochemical reaction mechanisms have been derived by several chemists (38-42). Current reversal chronopotentiometry and cyclic chronopotentiometry can be used for studying the kinetics of electrochemical reactions (43,44).

Chronopotentiometric techniques have not been widely used for the past 10 to 15 years. A major reason that they have not is that the electrode potential changes during a chronopotentiometric experiment and a fraction of the impressed current is always consumed by the charging of double layer capacitor and not by the Faradaic process. Theoretically, a chronopotentiogram rises very rapidly at

the beginning and at the end. However, due to the charging current which decreases the rate of the Faradaic process, the chronopotentiogram rises slower especially at the beginning and at the end where the rapid voltage change requires more charging current. The chronopotentiogram distortion makes the determination of transition time more difficult and less accurate when the concentration is below 10^{-3} M. Chronopotentiometric techniques can be useful if the effect of double layer charging can be removed.

The effect of double layer capacitance on the distortion of chronopotentiograms has been considered by several chemists (45-50). All authors have assumed that the double layer capacitance is independent of electrode potential. If one takes 10 seconds as the transition time, the lower limit for practical analytical applications is at about 10^{-4} M concentration leyel (51).

Several graphical methods (36,45,48,52-55) have been proposed to determine the transition time from a chronopotentiogram. However, none of these methods yield a constant value for $i\tau^{1/2}/C$ which should be constant according to the Sand's equation (Equation 4-1).

Several chemists have tried to generate a constant Faradaic current by adding the required charging current. L. Gierst (56) and P. Mechelynck (57) developed a device with a capacitor parallel to the electrochemical cell in the circuit. A capacitor with capacitance approximately equal

to the double layer capacitance of the working electrode was chosen for the device. The current needed for charging the capacitor was added to the constant current and the resulting current was impressed on the electrochemical cell. The major disadvantages of this technique are: 1. The correction of charging current is not exact because the double layer capacitance changes as a function of potential. 2. The device can not handle chronopotentiometric experiments with short transition time (below a few seconds).

W. D. Shults and co-workers (58) developed an apparatus with a function similar to the ones developed by L. Gierst (56) and P. Mechelynck (57). Instead of a capacitor, they used a blank cell with the same electrode area and with a solution containing only the supporting electrolyte. The change of double layer capacitance with potential can be taken into account by this method.

The apparatus developed by Gierst and Mechelynck and by Shults tend to cause the problem of overshooting due to the positive feedback. When there is some small time delay in the addition of charging current, the reduction of Ox will be subject to a suddenly increased current which causes a smaller transition time than there should be. Shults and co-workers used their apparatus only for long transition time such as 6 - 40 seconds. According to W. H. Reinmuth (59), these apparatus can only be applied to long transition

time because of the inherent instability.

P. Bos and E. Van Dalen (60) developed an apparatus based on the same principles as the one of W. D. Shults and co-workers. The apparatus was protected against overloading by the charging current. The shortest transition time can be as short as 0.1 second. With the transition time at about 11 seconds, the former obtained fairly constant $i\tau^{1/s}/C$ values (1.9 % RSD) for a mercury electrode in T1⁺ and KNO_s solution with T1⁺ in the concentration range between $3.25 \cdot 10^{-5}$ M and $6.5 \cdot 10^{-4}$ M. There are still some disadvantages in using this method: 1. Two cells must be used. 2. The areas and the capacitances of both electrodes. 3. The shortest transition time (0.1 second) which can be applied is not very short.

C. Description and Test of the Nethod

In this section a method for obtaining a constant Faradaic current based on a charge injection method will be discussed.

The capacitive charge has been given this name because it is directed to the double layer capacitance. The charge required to change the potential of an electrode from one fixed potential to any other potential will be designated as an integral capacitive charge. Before the charging current compensation is accomplished, the integral capacitive















Figure 4-1.

Illustration of the compensation of charging current, the first part.



Figure 4-1.

•

Illustration of the compensation of charging current, the second part.

charges of the working electrode must be measured over the potential range of interest. The results are stored in the memory of the computer. Figure 4-1b illustrates the integral capacitive charge Q_{int} required to make the potential perturbation from the potential E_{init} to the potential E for a working electrode in a solution. The solution may be a blank solution containing only the supporting electrolyte and the solvent, or it may be the solution to be tested. Although the integral capacitive charge measurement may be subject to some error when the test solution contains electroactive materials, the error is small for low concentrations of electroactive species as will be shown in Figures 4-14 and 4-15 (less than 11 % error for $2 \cdot 10^{-5}$ M Cd⁺² and less than 4 % error for $5 \cdot 10^{-6}$ M Cd⁺² in 0.01 M KC1 solution).

Figure 4-2 shows the process of measuring the integral capacitive charge. The operator sets up the parameters such as initial potential E_{init} , final potential E_{final} , charge to be injected Q_i , delay time for charge injection t_d , etc. For the reaction $0x + ne^- \Rightarrow \text{Red}$, E_{final} is more negative than E_{init} and Q_i is negative. After the operator starts the program, the PCP measures the electrode potential and compares it to the desired initial value E_{init} . If there is any difference in these two potential values, the PCP injects enough charge pulses of proper polarity to bring the electrode potential to the initial value. After the initial

and the second second



Figure 4-2. Flow chart of the program for the measurement of integral capacitive charge; the first part



Figure 4-2.

Flow chart of the program for the measurement of integral capacitive charge, the second part potential is reached, the PCP reports to the operator that it is ready to execute data acquisition. The PCP keeps the electrode potential at E_{init} by adding charge pulses as needed until the operator commands the PCP to execute the data acquisition. After the data acquisition starts, the PCP makes sure that the potential E_{11} before charge injection is equal to E_{init}, then injects the charge Q_i to the electrode and measures the potential E_{21} after some slight delay time t_A. The minimum time required by the computer programming is about 50 µs for each charge injection. The PCP calculates the potential difference E_{d1} resulting from one injection of charge Q_i by the equation: $E_{d1} = E_{21} - E_{11}$ (E_{d1} should have a negative value). The integral capacitive charge Q_{int} for the individual value of potentials between E_{init} and E_{init} + E_{d1} such as E_{init}, E_{init} - 1.22 mV, E_{init} - 2.44 mV,... $E_{init} + E_{d1}$, is calculated by assuming that Q_{int} is linear within the potential range. The results are stored in the memory of the computer. On the screen, the PCP reports the values E_{11} and E_{21} , the potentials before and after charge injection. A similar nth process is performed to bring the electrode potential to E_{init}, to inject charge Q; n times, to measure the electrode potential after charge injections E_{2n} , to calculate the potential difference $E_{dn} = E_{2n} - E_{1n} (E_{1n} = E_{init})$, to calculate the individual capacitive charge for the potentials within the range $E_{init} + E_{dn-1} - 1.22$ mV and $E_{init} + E_{dn}$, and to report

the values E_{1n} and E_{2n} , the potentials before and after charge injections. When the process has been completed for a potential which is more negative than $E_{final} - 24.4 \text{ mV}$, the PCP reports the value N, the number of charge injections that have been performed during the most recent series of charge injections. When this occurs, the measurement of the integral capacitive charge has been completed. The capacitive charge for the individual potential between E_{init} and E_{final} - 24.4 mV such as E_{init}, E_{init} - 1.22 mV,..., $E_{final}, \ldots, E_{final} - 24.4$ mV, is stored in the memory of the microcomputer during the execution of this program. In order to provide a wider range of capacitive charge memory file, the value $E_{final} - 24.4$ mV is selected to be more negative than E_{final}. To obtain fairly accurate capacitive charge for the individual potential, a value of 10 mV to 20 mV for the potential range between E_{dn-1} and E_{dn} is suggested for any electrode which have fairly constant value of double layer capacitance within the potential range (such as mercury electrode in 0.1 M or 0.01 M KC1 solutions).

For the compensation of charging current using a blank cell, the integral capacitive charge should be measured before testing the solution of interest. For the compensation of charging current in-situ, the integral capacitive charge may be measured after each chronopotentiometric scan.

To perform charging current compensation, the

operator prepares the solution to be tested and sets up the parameters such as the initial potential E_{init}, the final potential Efinal, the time interval between two potential measurements t_{d1}, maximum number of charge injections within the period t_{d1}, a number TIPD for the amount of delay time TIPD t_{d1} to keep the electrode potential at E_{init} after setting up the timer and before executing data collection, etc. The operator then initiates the program to perform charging current compensation. The process of performing the first chronopotentiometric scan is shown in Figures 4-1c, 4-1d, and 4-4. The PCP brings the electrode potential to the initial value E_{init} and informs the operator that it is ready to begin the data acquisition. The operator can start the data acquisition after this point has been reached; however, it will be found advantageous to wait for a while (e.g., 10 seconds) for the electrode to reduce any impurities which are present in its vinicity and are reducible at E_{init}. After the operator commands the PCP to start the data acquisition, the PCP sets up the timer, keeps the potential at E_{init} for a period TIPD t_{d1} . The PCP measures the electrode potential at the times t_a and stores it in the computer memory, injects a charge pulse or a number of charge pulses to the electrode, waits until t₁ is reached, then measures the electrode potential and injects another charge pulse or more charge pulses. The PCP continues to measure the electrode potential and to inject a



Figure 4-3. Flow chart of the program for chronopotentiometry with chargeing current compensation.



Figure 4-4. Flow

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Flow chart of the program for data acquisition of a chronopotentiogram

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constant quantity of charges within t_{d1} time interval until the electrode potential is equal to or more negative than E_{final} , or until 512 potentials are measured, whichever occurs first. The electrode potentials at the time t_0 , t_1 , t_2 , ..., etc. are stored in the memory during the data acquisition.

For the injection of a charge pulse between two adjacent potential measurements, the minimum value of t_{d1} is 156 µS. For the injection of two charge pulses, it is 205 µS. For the injection of 10 charge pulses, it is 601 µS. A detailed calculation of the minimum value of t_{d1} can be found in Appendix A.

After the first scan, in order to restore the desired initial concentration of Ox in the vinicity of the electrode surface, the electrode potential is brought to the value E_{init} to oxidize Red back to Ox for a reversible reaction, or not to reduce Ox any more for an irreversible reaction. This is necessary in the situation in which an HMDE is used and the same mercury drop will be used, or in the situation in which a solid electrode is used, because the solution near the electrode is not well stirred. After the potential reaches E_{init} , the chronopotentiogram, the E vs. time curve, will be plotted on the screen. The operator may save the results. The PCP holds the electrode potential at E_{init} . The operator may perform the capacitive charge measurement with the same solution when the in-situ compensation is

desired. It is advantageous to do so for in-situ compensation of charging current since the problem of drift of electrode capacitance can be reduced.

As shown on Figures 4-1 and 4-3 the PCP identifies the electrode potential E_n at the time t_n (Figure 4-1c) and locates the integral capacitive charge $Q_{int}(E_n)$ for the potential E_n (Figure 4-1b). The Faradaic charge $Q_f(t_n)$ (Figure 4-1a), which has been consumed at the time t_n , is calculated by the equation:

$$Q_{f}(t_{n}) = Q_{T}(t_{n}) - Q_{int}(E_{n})$$
 (4-7),

where $Q_T(t_n)$ is the total charge injected at the time t_n of the most recent scan (In the first scan, the Q_T vs. time curve is the desired Q_f curve).

There are many possible methods of adjusting the total charge curve in order to make the Faradaic charge curve approach the desired straight line. As shown on Figure 4-1e, a simple method is to add the charge difference between $Q_f(t_n)$ and the desired Q_f value to $Q_T(t_n)$ of the most recent scan. Instead of this simple method, I used a method which provides faster compensation. This method is illustrated in Figure 4-1f. The PCP locates the time t_m at which $Q_f(t_n)$ should have been consumed. For the next scan, the total charge at t_m will be the total charge which has been injected at t_n of the previous scan. The Q_T vs. t curve is extended to a higher quantity of charge by extrapolating the end portion of the curve.

If the potential E_{n1} (not shown) at the time t_{n1} is more negative than the potentials before t_{n1} , and if E_{n2} is the first potential which is more negative than E_{n1} , the capacitive charge corresponding to the potential change is divided evenly in the time period between t_{m1} and t_{m2} . $(t_{m1}$ and t_{m2} are determined in the same manner as t_m is determined.) The total charge curve is smoothed and then divided into small charge segments to be injected in the next scan as shown in Figures 4-1g and 4-1h. The smoothing is performed by using a small time window to average the data within it. If the capacitive charge is not distributed evenly within t_{m1} and t_{m2} , or if the smoothing is not performed, the injection of a large charge pulse or large charge pulses resulting from the compensation of charging current may generate a potential spike of a few millivolts in the next chronopotentiogram. A time window of less than 5 % of the transition time is suggested for smoothing. This kind of charging current compensation can be repeated a number of times until the Faradaic charge curve is close to the desired straight line or until the chronopotentiogram rises rapidly at the beginning and at the end.

This method of charging current compensation was tested with a dummy cell whose configuration is shown in Figure 4-5. The charge injected into the capacitor C_{dm1} was



Figure 4-5. Circuits of dummy cell for the test of charging current compensation for chronopotentiometry C_{dm1}: the charging capacitor C_{dm2}: the Faradaic capacitor considered as the capacitive charge which would be compensated. The charge injected into the capacitor C_{dm2} was considered as the Faradaic charge. With both capacitors in the feedback loop and after compensating for the charging current, the results should look as if only the capacitor C_{dm2} were present.

Figure 4-6 shows the voltage vs. time curve obtained by applying a constant current to the dummy cell with only the capacitor C_{dm2} in the feedback loop (only the switch B was closed).

The integral capacitive charge was obtained with only the capacitor C_{dm1} in the feedback loop. Both capacitors C_{dm1} and C_{dm2} were then put in the feedback loop and the compensation of the capacitive charge of C_{dm1} was performed. Curve 0 of Figure 4-7 shows the voltage vs. time curve obtained by applying the constant current to both capacitors and without any charging current compensation. The capacitive charge of the capacitor C_{dm1} was compensated in the subsequent scans according to the method described above. In Figure 4-7, the curve 1 corresponds to the results obtained in the first compensation the charging current, curve 2 corresponds to the second compensation, and so on. The fact that curves 1, 2, and 3 almost coincide suggests that the effect of the presence of the capacitor C_{dm1} in this cell configuration can be eliminated with just The fact that one compensation is one compensation.
DUMMY CELL



Figure 4-6. Voltage-time curve obtained with one dummy capacitor, C_{dm2}.

DUMMY CELL



sufficient for this cell configuration can also be predicted through simple geometry.

Curve 1 of Figure 4-8 (the same as the curve of Figure 4-6) shows the results obtained with only C_{dm1} in the feedback loop. Curve 2 of Figure 4-8 is equivalent to the curve 3 of Figure 4-7 and shows the results obtained with both capacitors in the feedback loop and with the compensation of the capacitive charge of C_{dm1} . Clearly, this method of charging current compensation removes the capacitance effect.

The application of this method to two chemical systems will be discussed in Sections D and E. Reagent grade cadmium chloride and thallium chloride were used. Reagent grade potassium chloride had been recrystallized once before its use as a supporting electrolyte. Solutions WOIC prepared with de-ionized water which had been passed through an ion-exchange column manufactured by Millipore. The time windows for smoothing the total charge curve was less than 5 % of the transition time for Thallium solutions and for cadmium solutions with concentration above 10^{-6} M. For lower concentrations of cadmium solution, time windows were less than 15 % of the transition time. Each solution was de-aerated with nitrogen gas which had been passed through a vanadous chloride solution to LOBOAG orygen. **A**11 experiments were performed at room temperature between 20.5 and 21.5 °C.

DUMMY CELL





D. Compensation of Charging Current for the System T1C1 in 0.1 N KC1 solution.

The method of compensation of charging current was tested with T1C1 in 0.1 M KC1 solution and with a sitting mercury drop electrode. The charging current Was compensated with a blank solution (0.1 M KCl). If the effect of charging current is removed, the value $i\tau^{1/2}/C_{or}$ should be constant according to the Sand's equation (Equation 4-1). As long as there is no adsorption of Ox (T1⁺), the presence of Ox (T1⁺) does not change the capacitance of the electrode, and proper experimental precautions have been observed (e.g., use a short transition time to avoid possible convection). Many chemical systems could be selected to test the method of charging current compensation for chronopotentiometry since the reversibility of the reaction Ox + ne * Red is not an important issue. The charging current has a more profound effect on the chronopotentiogram of one electron transfer reaction than on than of a multi-electron transfer reaction. T1⁺ is selected because its reduction involves only one electron transfer. The tested concentration range of $T1^+$ was between 10^{-3} M and 10⁻⁵ M. Since KCl and T1Cl are both uni-valent electrolyte and the concentration of KC1 (0.1 M) is at least 100 times higher than that of T1⁺, the selection of KC1 concentration should effectively remove the migration of T1⁺ as a result of changing the electrical field in the vicinity of the

electrode surface during a chronopotentiometric experiment.

To avoid the tedium of coating the contact for a hanging mercury drop electrode (HMDE), a sitting mercury drop electrode (SMDE) was used. A platinum wire of 0.01 inch in diameter sealed in soft glass was used to construct the sitting mercury drop electrode. One end of the soft glass and the sealed platinum was ground with 3 micron aluminum oxide then bent to a U-shape. The platinum wire of the other end was soldered with a copper wire which facilitated an electrical connection to the PCP. The ground platinum tip was coated with mercury according to Wang's method (61). This background current of the SMDE is not significant compared to the desired Faradaic current (the background current was 1.4 % of the desired Faradaic current at 10⁻⁵ N T1⁺ concentration level). A fresh mercury drop was obtained by transferring a fixed volume of mercury from an HNDE to the SNDE. The potential of the SNDE was kept at B_{init} for 10 seconds to reduce impurities before starting acquisition of data. The background current was the corrected by adding it to the desired Faradaic current.

Figure 4-9 shows the capacitive charge obtained with the SMDE in 0.1 M KCl solution with $t_d = 1.24$ ms (see Figure 4-2). Forty charge increments were used to measure the integral capacitive charge for the potential perturbation from -0.325 V to -0.725 V vs. SCE. The times required for this potential perturbation are 4.6 ms and 49.6 ms for t_d





Figure 4-9. Integral capacitive charge of SMDE in 0.1 M KCI solution

0.115 ms and 1.24 ms respectively. The curve of integral capacitive charge was not changed significantly by changing t_A (within 2.2 % for t_A between 0.115 ms and 1.24 ms).

Figures 4-10 and 4-11 show an example of the compensation of charging current for 10^{-4} M T1C1 in 0.1 M KC1 solution. Curve 0 of Figure 4-10 shows the chronopotentiogram obtained without any compensation of charging current. Curve 0 of Figure 4-11 is the Faradaic charge vs. time curve of the first scan (without any charging current compensation). Curve 1 of Figure 4-10 shows the chronopotentiogram obtained with the first compensation of charging current. Curve 1 of Figure 4-11 is Faradaic charge curve corresponding to the first the compensation. Curve 2 of Figure 4-10 shows the chronopotentiogram obtained with the second compensation, etc. After a few compensations, the transition time approached a constant value.

Table 4-1 lists the transition times corresponding to the number of charging current compensations for a number of concentrations. In each case, the same i/C value of 1.02 $A \cdot cm^{-3} \cdot mole^{-1} \cdot liter$ was used. The transition times were obtained by the graphical method of Delahay and Nattax (36,54). The method is demonstrated by the graphing and finding of the time interval corresponding to the line segment AB for curve 0 of Figure 4-10. After a few compensations, the transition time approached a constant

POTENTIAL V8. SCE (VOLTS)

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 10^{-4} M TICI in 0.1 M KCI



Figure 4-10. Chronopotentiograms of 10⁻⁴ M TICI solution Curve D: No charging current compensation Curve 1: the first compensation Curve 2: the second compensation Curve 3: the third compensation

 10^{-4} M TICI in 0.1 M KCI



Figure 4-11.	Faradaic charge curves for SMDE in
	10 ⁻⁴ M TICI and 0.1 M KCI solution
	Curve D: no charging current compensation
	Curve 1: the first compensation
	Curve 2: the second compensation
	Curve 3: the third compensation

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Table 4-1. Transition times (seconds) of various concentrations of TlCl as a function of number of charging current compensations

Number of compensations	10-*	5·10 ⁻³	2 • 10 - 4	10-4	5·10 ^{-\$}
0	0.121	0.126	0.128	0.133	0.145
1	0.111	0.110	0.109	0.113	0.112
2	0.112	0.108	0.108	0.112	0.106
3	0.112	0.108	0.108	0.112	0.106

Figure 4-12 shows a plot of $i\tau^{1/2}/C$ vs. log concentration of T1⁺ with and without charging current compensation. The transition times with charging current compensation were the constant values achieved after scans. The averaged transition time for each concentration was obtained by taking the average of 9 transition times. These 9 transition times were obtained from 3 solutions with 3 transition time from each solution. It is clear that the plot of $i\tau^{1/3}/C$ vs. C is more dramatically constant with charging current compensation. TICI in 0.1 M KCI



Figure 4-12.

 $i \tau^{1/2}/C$ vs. log concentration (M) of TI⁺ o, with charging current compensation x, without charging current compensation

e C \$ ¢ (\$ (¢ 8 E_i \$ c to ¢h th 11 the ele E. In-Situ Compensation of Charging Current as a Sensitive Technique for the Detection of Cd⁺³

In order to test the sensitivity of the method, longer transition times were used to enhance the ratio of the Faradaic to the capacitive charges. Transition times between 1.6 sec and 7.5 sec were used. These transition times were kept short in order to avoid the effect of natural convection. Concentrations of cadmium solutions between $2 \cdot 10^{-5}$ M and $2.5 \cdot 10^{-7}$ M in 0.01 M KC1 solution were tested with an HNDE. A low concentration of supporting electrolyte solution of 0.01 M KCl was used in order to decrease the introduction of impurities with the supporting electrolyte. In order to avoid the uncertainty of integral capacitive charge due to the variation of electrode area, a single mercury drop was used for each solution in both capacitive charge measurements and chronopotentiometric scans (This could be done because the concentration of cadmium is low). The electrode potential was brought to Einit (-475 mV vs. SCE) after each chronopotentiometric scan. The electrode potential was then kept at E_{init} for 10 to 20 seconds before measuring the integral capacitive charge of the electrode in the same solution. It was found that if the electrode was kept at E_{init} for a time as short as 2 seconds, the measured capacitive charge is essentially the same. After the capacitive charge was measured, the electrode potential was held at E_{init} for 3 minutes before starting the next chronopotentiometric scan for those scans whose transition time almost approached a constant value. For the first few scans, only one minute time interval was used in order to save time. The purpose of doing this is to strip out most of the cadmium which had been deposited on the mercury drop.

Figure 4-13 shows the chronopotentiograms obtained with $2 \cdot 10^{-5}$ M CdCl₃ in 0.01 M KCl solution. A desired Faradaic current density of 3.5 μ A/cm³ was used. Curve 0 shows the results obtained without any compensation of charging current. Curve 1 shows the results obtained with the first compensation of charging current, curve 2 chows the results obtained with the second compensation of charging current, etc.

The integral capacitive charge of the HMDE in the solution containing $2 \cdot 10^{-5}$ M CdCl₃ and 0.01 M KCl decreases about 2 % during the time interval between two adjacent chronopotentiometric scans. The rate of decrease is so small that it does not introduce significant error in the charging current compensation.

For the capacitive charge measurement, between 20 and 25 charge increments (see Figure 4-2) were used to change the electrode potential from -475 mV and -775 mV vs. SCE. The time required to make the potential perturbation is between 1.3 ms and 1.6 ms which is considerably less than the transition time used (between 1.7 and 7.4 seconds); $2*10^{-5}$ M CdCl₂ in 0.01 M KCl





therefore, the Faradaic charge consumed in the time interval for capacitive charge measurement is much less than the Faradaic charge consumed in the chronopotentiometric experiment.

Figure 4-14 shows the comparison of the measured integral capacitive charge obtained with a fresh mercury drop in 0.01 N KCl solution (curve 1) and in $2 \cdot 10^{-5}$ N CdCl₂ and 0.01 N KCl solution (curve 2). The fact that the measured integral capacitive charge is higher for cadmium solution than for plain supporting electrolyte solution is principally due to the reduction of some cadmium ions during the period of capacitive charge measurement.

For lower concentration of cadmium solutions, the error involved in the capacitive charge measurement due to Faradaic reaction is less. For example, the integral capacitive charge measured with $5 \cdot 10^{-6}$ M CdCl₃ has a lower error as shown in Figure 4-15. Even with the solution of $2 \cdot 10^{-5}$ M CdCl₃ in 0.01 M KCl, it is still advantageous to compensate for the charging current because the charging current is still the major error.

Figure 4-16 shows another example of in-situ charging current compensation. The test solution contained 10^{-6} M CdCl₃ and 0.01 M KCl. A steady state background current of 0.017 μ A/cm³ (10 % of the desired Faradaic current) was determined at E_{init} (-475 mV vs. SCE). The background was corrected by adding it to the Faradaic current of 0.17





Figure 4-14.

Measured integral capacitive charge for 0.01 M KCl solution Curve 1: without $CdCl_2$ Curve 2: with $2 \cdot 10^{-5}$ M $CdCl_2$ 0.01 M KCI W O $5*10^{-6}$ M CdCl₂



Figure 4-15.

Measured integral capacitive charge for 0.01 M KCl solution Curve 1: without $CdCl_2$ Curve 2: with 5 10^{-6} M $CdCl_2$ 10^{-6} M CdCl₂ in 0.01 M KCl



Figure 4-16. Chronopotentiograms of 10⁻⁶ M CdCl₂ The number besides the curve corresponding to the number of charging current compensation

 $\mu A/cm^3$. Curve 0 shows the chronopotentiogram obtained without any charging current compensation. Curve 1 is obtained with the first compensation of charging current, curve 2 is obtained with the second compensation, etc.

Figure 4-17 shows a chronopotentiogram of $5 \cdot 10^{-7}$ M Cd⁺² obtained after the transition time approached a constant value.

Table 4-2 lists the desired Faradaic current i_f applied to the electrode, the transition time τ , $i\tau^{1/3}$, and the ratio of background current i_b to i_f for a number of concentrations.



Figure 4-17.

Chronopotentiograms of $5 \cdot 10^{-7}$ M CdCl₂ with charging current compensation

M Cd ⁺²	2.10-5	1.5.10-5	10-5	5.10-6
i (µA/cm²)	3.5±4%	2.6±4%	1.7±4%	0.87±4%
i _b /i _f ·100% ·	0%	0%	0%	0%
τ (sec)	6.2	6.5	6.6	7.4
iτ ^{1/3} (μA·sec ^{1/3} /cm ³)	8.6	6.6	4.5	2.4

Table 4-2. List of i_f , i_h/i_f , τ , $i\tau^{1/2}$ for CdCl₂ solutions.

₩ Cd ^{+s}	2.10-6	10-6	5.10-7	2.5.10-7
i (µA/cm²)	0.35±4%	0.17±4%	0.17 <u>+</u> 4%	0.087±7%
i _b /i _f ·100%	5%	10%	10%	30%
т (зес)	7.2	6.1	1.7	2.2
iτ ^{1/3} (μΑ·sec ^{1/3} /cm ³)	0.93	0.43	0.22	0.13

Figure 4-18 shows a plot of $i\tau^{1/2}$ vs. concentration of cadmium. Theoretically, $i\tau^{1/2}$ should be proportional to the concentration according to Equation 4-1. A fairly linear $i\tau^{1/2}$ vs. C relationship was obtained.

F. Discussion

The method of charging current compensation can effectively remove the effect of double layer charging as it was shown in the previous two sections. The potentials are monitored while there is no current passing through the solution. As a result of this, potential measurements can be made more accurately, and the solution resistance causes ,

CdCl₂ in 0.01 M KCl



Figure 4-18. $i\tau^{1/2}$ vs. concentration of Cd⁺²

fewer problems. Therefore, this method can be applied to solutions of non-aqueous solvents, which may not be able to dissolve much electrolyte (the concentration of electrolyte may be as low as 0.1 mN).

According to Bos and Van Dalen (60), for the reaction $Ox + ne^- \Rightarrow Red$, it is advantageous to keep the electrode potential at a value which is somewhat (e.g., 100 mV to 200 mV) more positive than the E^{\bullet} of the redox couple to reduce impurities which react rapidly at this potential. They used a potentiostat to control the potential before the cell was switched to an amperostat. The PCP serves both as a potentiostat and an galvanostat. Switching from the controlled potential mode to the controlled current mode by the PCP is practically instantaneous and does not cause any problems such as a current spike.

Bos and Van Dalen's apparatus (60) can perform charging current compensated chronopotentiometric experiments whose transition times are as short as 0.1 second. The method this in thesis work handle. developed CIN chronopotentiometric experiments at least 10 times faster. For example, if 30 potential measurements are desired, the minimum time required for taking these data is

$$0.156 \text{ ms} \cdot 30 = 4.7 \text{ ms}$$
 (4-8),

where 0.156 ms is the minimum time required between two

adjacent potential measurements.

If the Bos and Van Dalen's device is used for charging current compensation, two cells must be used. One of them contains the solution of interest. Another is the blank cell which contains solution of supporting electrolyte only. The blank cell is used for generating charging current. The areas and the surface conditions of the working electrodes in these two cells must be identical; otherwise, the compensation will be subject to error. When solid electrodes are used, it is difficult to match electrodes because of the surface roughness, the adsorption, and the oxide film formation.

With the technique of in-situ compensation of charging current developed in this thesis work, the capacitive charge was measured with the same electrode in the same solution. The effort of preparing a blank cell can be saved. When the solution does not contain high concentrations of electroactive species (upper limit about $2 \cdot 10^{-5}$ M), the charging current can be compensated more accurately with method since the capacitive charge of the same this electrode is used. The drift of the quantity of measured capacitive charge was not a serious problem for mercury electrode since the time interval between two adjacent chronopotentiometric scans was four minutes or less. A drift of less than 2 % was observed in that time interval. The time interval can be shortened to enhance the speed of

analysis or to reduce the drift of capacitance value in the following situations: 1. A shorter transition time is used. (smaller diffusion layer thickness makes it quicker to restore the initial Ox concentration.) 2. A solid electrode is used and with knocking the electrode after each scan (to restore the initial Ox concentration at the electrode surface).

With the technique of in-situ compensation of charging current, the effect of adsorption of non-electroactive materials on the capacitance measurement can be taken into account since the capacitive charge is measured with the same electrode between two chronopotentiometric scans. In the situation in which the electroactive species is adsorbed on the electrode and it reacts rapidly, the charge consumed by this Faradaic reaction is included in the measured capacitive charge. The measured capacitive charge will be compensated in the next scan. Thus, the effect of the adsorption of this type of electroactive species is removed. In this situation, the method only takes the diffusion of electroactive species into account. As a result of this, it gives a better analytical working curve. These reasons, discussed above, probably explain why this technique improves the sensitivity of chronopotentiometry by more than one order of magnitude. The sensitivity of this technique is similar to that of pulse polarography.

The major limitation of the PCP is that high current

densities cannot be employed. For example, in the case of a chronopotentiometric experiment, current densities above mA/cm³ range cannot be applied. The program for performing a chronopotentiometric experiment involves reading the DAC values from the computer memory, calling a program to inject charge pulses, calling a program to measure the potential, etc. All of these processes require time. When high concentrations of supporting electrolytes are used (e.g., above 0.01 N), the speed of operation of the PCP is mainly limited by the speed of the microcomputer. The maximum current which can be applied to an electrode is limited by the speed of the PCP. A large potential perturbation, due to each charge injection, is certainly not desirable in any chronopotentiometric experiments. If 3 mV is set as the tolerable potential perturbation resulting from a charge injection, and if the electrode capacitance is $20 \ \mu F/cm^3$, the maximum current that can be applied to the electrode is

$$(3 \text{ mV}) \cdot (20 \mu\text{F/cm}^3) / (156 \mu\text{s}) = 0.38 \text{ mA/cm}^3$$
 (4-9),

where 156 μ s is the minimum time required between two adjacent voltage measurements. If higher current densities are desired, a faster computer or a hardware charge injection and measurement system should be used.

CHAPTER 5

SOME POSSIBLE APPLICATIONS OF THE PCP

Several operational modes of the PCP have been discussed in Chapter 2. In this chapter, some possible applications of these operational modes will be discussed. Nost of them have not been investigated. However, the expected performance will be discussed at the best of my knowledge.

A. The applications of the controlled charge mode

1. Traditional Coulostatic Method

A large charge pulse can be applied to the working electrode in order to bring the electrode potential to a value where the species of interest will react. The potential can then be monitored as a function of time. The decay of potential can be related to the kinetics of the reaction (12,62) or the concentration of the electroactive species (8,11,14,63). The time required for the voltage measurement system (VMS) is 8 µs. The minimum sampling time required for each data point is between 20 and 25 µs if the time required for commanding the VMS to convert data, for reading the converted data, and for storing the data are included. The voltage sampling speed is moderate. For very fast reactions, the PCP is not suitable. The resolution of

VMS is 1.22 mV which should be adequate for most experiments. The DAC has 10-bit resolution and is bipolar. The uncertainty in output voltage for each digital data is $\pm 1/2$ LSB which corresponds to ± 2.5 mV. The relative error of charge can easily be less than 1% if proper voltage of DAC is selected (e.g., more than 0.5V out of the full range 2.5V).

2. Electrode Capacitance Neasurement

For the measurement of differential capacitance, as discussed in Chapter 3, the PCP can inject an amount of charge appropriate for the desired potential perturbation (e.g., 10mV). The entire capacitance-voltage profile of an electrode can be obtained in as little as 8 seconds. The capacitance measurement is very accurate since the calibration error of the ADC is reduced (see Chapter 3) and the operator can select the number of iterations of measurement in order to attain the desired precision. Usually, the experimental technique is the main source of error.

For the measurement of integral capacitance, a method has been discussed in Chapter 4 which requires a few milliseconds for a potential perturbation of 0.4V. A faster program could be developed which would reduce the time to about 50 μ s. If only one measurement is performed for each potential and if the potential perturbation is large (e.g., more than 100mV), the relative error in capacitance

measurement is not large since the potential uncertainty is only 1.22 mV.

3. Coulometric Titrations

Precise amounts of charge can be used to generate titrant electrochemically. The electrode potential can be monitored as a function of time or as a function of charge added.

A four electrode system can be employed with a very small change to the basic electronic circuitry. The current applied between the working and counter electrodes generates the desired titrant at the working electrode. In order to prevent the reaction products at the counter electrode from getting into the sample solution, the counter electrode can be separated from the sample solution by a fritted glass. A simple differential amplifier is needed. The reference electrode and the indicator electrode can be connected to the inputs of this differential amplifier and the output of the differential amplifier can be connected to the VMS. Using a computer controlled system such as the PCP, a high current can be applied to generate titrant very quickly at first and then the current or the rate of charge injection can be decreased around the end point to allow the solution well mixed. By doing this, the analysis time can be shortened. A computer based instrument such as the PCP also provides advantages in the continuous monitoring of analyte, some of these advantages are automatic current range

selection, recording and warning. In the monitoring of olefins and surfur compounds in petroleum using coulometric titration, electrochemically generated bromine can be added to petroleum streams (64). The bromine in the solution reacts with the sample components and causes a decrease in the concentration of bromine. The PCP can adjust the rate of generating of bromine in order to maintain its concentration at a specific level. One problem in using a conventional coulometric titrator is that the current applied between the working and counter electrodes generates an IR drop between them. The reference and indicator electrode should be positioned on an equal potential This consideration contour. caused by simultaneous electrochemical generation and detection has prompted the use of spectrophotometric end point detection in some applications of conlometric titration. Using the PCP, the current between working and counter electrode is zero at the moment when potential is sampled so the problem of IR drop would be eliminated.

B. The Application of the Controlled Current Mode

1. Constant Current Chronopotentiometry

The application of PCP to constant current chronopotentiometry has been discussed in Chapter 4. A major advantage is that it is free from IR drop.

2. Programmed Current Chronopotentiometry

An instrument has been constructed by a group of electrochemists (65) for chronopotentiometry with current that varies along with any selected power of time, t^q . The transition time τ , which is the time interval between two sharp potential changes, is related to the concentration C by

$$\tau^{(q+1/2)} = kC$$
 (5-1),

where k is a constant.

The advantage of this method is that a wide range of concentrations can be measured on a convenient time scale.

With PCP, a simple method to obtain a programmed current is to vary the quantity of charge to be injected to the electrode and keep the time interval between two adjacent charge injections constant. The main disvantage using PCP is that a high current density (above about 1 mA/cm²) cannot generally be employed partially due to the inherent complication of the programming.

3. Electrode Pretreatment

Sometimes a clean and a reproducible electrode surface can be obtained by heavily anodizing (e.g., 1 sec at 10 to 100 mA/cm^3) the electrode surface to remove adsorbed material. This current density for this application is not anticipated to be a problem since the programming is not as
complicated and the potential variation of the electrode is not as vital as that of controlled current experiments. The electrode's oxide layer is then reduced at a potential sufficient to remove it, but neither the hydrogen ions or the solvent are reduced. The pretreatment cycle is repeated before every experiment, usually in the test solution.

Through the use of the PCP, complex current or potential waveforms with precise timing control can be applied to the electrode for pretreatment. The experiment following the pretreatment can be carried out with the same instrument and can be completed automatically. This special capability results from the PCP's ability to switch polarization modes without disconnecting the cell or causing any undesirable electrical stress at the working electrode.

C. The Application of the Controlled Potential Mode

Virtually all kinds of controlled potential experiment (e.g., cyclic voltammetry, pulse polarography, chronocoulometry, rotating disk voltammetry, etc.) can be performed using the PCP.

For pulse polarography, the PCP can usually attain a potential step within the resolution of VMS (1.22 mV) in a few milliseconds. This is not extremely fast compare to most commercial instruments although most of them sample the current between 30-50 ms after the pulse. For solution of lower conductivity, the PCP has advantages over most commercial instruments since it is applicable to these solutions. One disadvantage of the PCP is that the resolution of the VMS is not high. A certain amount of electroactive species must be reacted before the PCP can detect it. The lower limit of detectable concentration depends on the technique applied. Generally, The PCP could be well suited for concentration of electroactive species above 10^{-7} M for direct analysis (For anodic stripping voltammetry, much lower concentration can be achieved.). If a lower detection limit is desirable, a higher resolution ADC could be used. However, there may not be much advantage to do so. In most cases, the background current is about the same level as the Faradaic current generated by this low concentration of electroactive species.

The PCP may have great advantages in the electrochemical detection for liquid chromatography. Because the coulostatic method does not require high solution conductance, the PCP can be well suited for a wide range of solutions. The differential pulse detection for liquid chromatography can eliminate some electrode fouling problems (66). The PCP can be well suited for this application since it can step potential faster than most commercial instruments under the condition of high solution resistance.

One favorable aspect of the PCP for controlled potential experiments is that the charging current can be

integrated over a short period of time (e.g., 2 ms) after a potential step. The resulting information may be used to study either electrode capacitance or surface adsorption. However, because some Faradaic reaction will occur during this period, the PCP can not be applied to this method of studying surface phenomena accurately if the concentration of electroactive species is roughly above 10^{-5} M. For small potential steps, the measured capacitive charge is subject to significant error because the resolution of VMS is 1.22 mV which may be a large fraction of size of the potential step.

D. The Applications of the Open Circuit Mode

Under the open circuit condition, the PCP can be used for the potentiometric measurements of PH electrode or other ion selective electrodes.

In normal pulse polarography, a potential waveform with potential pulses superimposed on a constant potential is applied to a working electrode. An operator with little experience may sometimes select an initial potential at which the electrode will dissolve, or the solvent will decompose, or the electroactive species of interest will start to react. If the potential in the open circuit condition is used as the initial potential, these problems can be avoided. E. The Applications of the Combination of Nodes

The PCP can switch from one mode of operation to another without any difficulty. The switching is achieved by software. Also, it is fast and produces no electrical stress on the working electrode.

Examples of possible application can be as follows:

The PCP can keep the working electrode potential at a desired value (controlled potential mode) then automatically switch to controlled current mode. During the controlled potential period, impurities easier to react than the species of interest can be consumed at the electrode surface. Thus, the interference of impurities can be reduced.

In controlled potential experiments, the information about the current can be the basis for some control decisions. For example, in cyclic voltammetry, if the current drops to a certain fraction of the peak current, the potential can start scanning in the reverse direction. Another example is for the preparations of desired concentrations of electrochemical species at the electrode. For example, the potential of the electrode can be kept at a value to achieve the desired initial concentration before switching to an experiment which could be a controlled charge experiment such as traditional coulostatic experiment. The amount of current at the potential for preparing the desired concentration can be used for the

decision of switching to the experiment. With the PCP, a series of experiments of this kind with different initial condition can be performed automatically.

The PCP can keep the potential of the working electrode at a desired value (controlled potential mode), then inject a charge pulse to the electrode either for capacitance measurement or for traditional coulostatic method (controlled charge mode).

In the compensation of charging current for chronopotentiometry which has been discussed in chapter 4, controlled charge mode is used to measure the capacitance of the working electrode. The measured capacitance is used in the controlled current mode to perform constant current chronopotentiometry with charging current compensation.

For normal pulse polarography, the open circuit mode can be used to obtain the potential at zero current. The potential may be used as the potential before voltage pulses are applied (controlled potential mode).

For charge step polarography (14), the potential of the electrode can be held at a value (controlled potential mode) where the electroactive species do not react before injection of increasing quantities of charge pulses (controlled charge mode). The slope of the potential decay after each charge pulse is injected can be monitored and plotted as a function of potential.

N. A. Schreiber and T. A. Last developed a computer

controlled coulostat (67). The basic design of their patterned after the one instrument is used by S. E. Hourdakis and C. G. Enke (18). They performed linear sweep anodic stripping voltammetry with a thin film mercury/wax-impregnated graphite electrode. The linear potential scan was achieved by injection of calculated quantity of charge at constant rate to keep the linear potential scan. The potential decay after each charge pulse injected was used to calculate the Faradaic component of the injected charge. The detection limit was 100 parts per trillion for cadmium.

The PCP can be used as a drop fall detector for dropping mercury electrodes as it was performed by Spyros Hourdakis (18). The idea is to inject a proper fixed quantity of charge to the electrode and measure the electrode potential difference before the charge injection and after the charge injection. The area of a drop of mercury is very small immediately after the previous drop falls. The potential change due to the injection of the charge pulse is more when the drop size is small than it is when the drop size is large. A sudden change of potential difference can be used for the detection of the time at which a drop falls.

The applications mentioned above are only a few of the many possible applications. Any complex potential or current waveform or any combination of the operational modes which will give the best response to the chemical system can be used either for electroanalytical work or for fundamental electrochemical research.

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CHAPTER 6

DESCRIPTION OF THE SYSTEM HARDWARE

A. Modification of the Charge Generator and the Voltage Neasurement System

Figure 6-1 shows the circuits of the Digital-to-Analog Converter (DAC) of the charge generator. The AD 7522 chip is a 10-bit converter manufactured by Analog Devices. The DAC is bipolar with maximum output voltage of \pm 2.5 V. The output voltage V is converted to - V by the inverter A4 so that both polarities of the voltage are available simultaneously. The circuits have been modified from S. Hourdakis' original design to provide faster responses for the application of this thesis work. The capacitor C1 has been changed from 560 pF to 150 pF and the capacitor C2 has been changed from 56 pF to 150 pF. These changes give the amplifier A1 a faster response. The output of A1 can come within 1/2 LSB (2.44 mV) of any output voltage in 9 μ s compared with 35 µs in the original design. Resistors R6 and R7 have been changed from $10K\Omega$, 5% to $25K\Omega$, 1% to provide better accuracy for the inverted voltage -V. A capacitor C3 of 50 pF has been added in the feedback loop of A4 to improve the signal.

Figure 6-2 shows the modified circuit of the Analog-to-Digital Converter (ADC). A resistor R5 of $1K\Omega$ is



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The bipolar Digital-to-Analog Converter Figure 8-1.



The voltage measurement system Figure 6-2.

added betwween the output of amplifier A2 and the 5.1 V Zener diodes to limit the output current for A1. A capacitor C1 of 100 pF is added in the feedback loop of A2 to reduce noise. These changes make the noise level very low without excessive reducing in the response speed. When the concentrations of KC1 solution are above 0.01 M and the electrochemical cell has not been shielded. the noise level of the electrode potential is below 0.1 mV which is much less than the resolution of the VMS (1.22 mV). For very low concentration of supporting electolyte such as 10^{-5} M KCl solution. a shield on the electrochemical cell is required to reduce noise level below 1.22 mV. The switch SWA is added between the non-inverting input of the amplifier A2 and the 12 V Zener diodes. When a very dilute electrolyte solution (such as below 10^{-5} M) is used. SWA can be kept open to prevent leakage current through the Zener diodes.

B. Interface Circuits

The circuits that interface the microcomputer to the DAC and the ADC provide the necessary digital signals to control them. Figure 6-3 shows the interface logic. Connections A6-A9 are address lines. Signal \overline{BS} is the board select signal for the charge generator and the voltage measurement system. When A9 is high and \overline{BS} is low, the 74LS138 decoder chip is selected. Under this condition, one of the outputs of 74LS138 can be selected (active low)



Interface logic for the DAC and the ADC Figure 6-3.

through proper signal levels on lines of A6-A8. Each output (Y0-Y5) of the 74LS138 chip is gated with \overline{WR} or \overline{RD} signal to make sure that the address lines are stable in order to provide a proper control signal. DAC LBS is for the loading the low byte (8-bit) data to the input buffer of the DAC. DAC HBS is for the loading of the high byte (2-bit) data to the input buffer of the DAC. The low byte should be loaded The DAC HBS also triggers a monostable (74LS221) to first. generate a 5 µs duration pulse for loading the DAC register from DAC input buffer. The falling edge of ADC START triggers the ADC to begin the conversion. The rising edge of the ADC START triggers a D flip-flop (74LS74) to hold the voltage which the ADC is converting. The ADC LBEN enables the reading of low byte (8-bit) data of the ADC. It also clears the Q output of the 74LS74 to change the S/H chip to the sample state. The ADC HBEN enables the reading of the high byte (4-bit) data of the ADC. The ADC BOCEN enables the user to check the status of the end of conversion signal of the ADC. These control signals are connected to the DAC and ADC boards. Letter w indicates a connection to the wire wrap side of the board, and letter c indicates a connection to the component side of the board. The number which follows the letter c or w gives the pin number.

The switch decoding circuits are shown in Figure 6-4. The microprocessor can turn on the desired switch by writing proper digital data to the latch (74174). Only one switch



Figure 6-4. Switch decoding circuits

can be turned on at any time.

When the proper address is sent to the decoder chip (74LS138) for programming the switches, the output (Y6) of 74LS138 goes low. The output is gated by the \overline{WR} signal to make sure that the address lines are stable and the computer is sending data. The duration of the low pulse of the output of the OR gate is about 500 ns. This signal is inverted by the NAND gate. The 74174 chip is used to latch data when the microprocessor programs the switches. The 7442 decoder chip selects one of the outputs through the input data. Only one switch is active (active low) at any time. The tri-state driver (74LS241) is used to drive the switch controlling signals and to turn its own output off when the data is being loaded into the the latch (74174). When the inputs of 7442 change, its outputs also change. The transitions of the outputs of 7442 is gated off by the tri-state driver in order to avoid the situation that a switch is being turned on while another switch is being turned off. The 74LS221 monostable multivibrator chip is used to generate a delayed (about 150 ns) low-to-high transition of the output (pin 8) of the NAND gate. This delayed transition makes sure that the data (DO-D3) are correct at the time of loading the latch. The delay (150 ns) is much shorter than the gating off time of the 74LS241 The outputs of 7442 will be stable before the gates chip. of 74LS241 are turned on again.

A power-up clear and a switch for hardware clear are connected to the latch. The switch 0 is selected when the latch is cleared. The switch 0 is used to connecte a 1KO feedback resistor between the output and the inverting input of the charging amplifier (the amplifier for adding charge to the electrode) to protect the amplifier (see Figure 35 of S. Hourdakis's dissertation (18)).

C. The Real Time Clock

The real time clock is based on the AM 9513 system timing controller chip manufactured by the Advanced Micro Devices, Inc. The chip includes five general-purpose 16 bit counters. A variety of internal frequency sources and external pins may be selected as inputs for individual counters. Both hardware and software gating of each counter is available. The counters may be programmed to count up and sown in either binary or BCD. The usage of this chip allows complex timing controll to be feasible.

In order to make the programming easy, a crystal of 1.000000 MHz is selected to generate an accurate counting source. The data bus lines (DBO-DB7) and \overline{RD} , \overline{WR} inputs of AM 9513 are connected to the microcomputer bus. The \overline{CS} of AM 9513 comes from one of the UNQUAL (not gated with \overline{WR} or \overline{RD}) outputs of the \overline{CS} board designed by Bruce Newcome (19). Each pin of the outputs (OUT1-OUT5), of the source inputs (SRC1-SRC5) and of the gate inputs (GATE1-GATE5) is connected to 4 patch wire sockets to enable interconnection between them or to other sources. A 50 pin connector is soldered on the real time clock board to make any connection to any device which needs accurate timing control or provides pulses to be counted.

The usage of AM9513 and the design of the real time clock board allow a flexible timing control for most electrochemial experiments. Detail programming of the timing controller and the connections between the outputs, sources and gates can be found in the programs TICP.MAC and TIM12.MAC in Appendix B.

APPENDIX A

Equation and Some Examples for the calculation of minimum values of t_{d1} for Chronopotentiometric Experiments

M is the number of charge injections between two adjacent potential measurements. N is the number of extra software loop for charging the capacitor and injection of charge to the electrode (N = SWDLY1 + SWDLY2 - 2). See programs CHGADN.MAC and SWDLYB.MAC in Appendix B. Minimum $t_{d1} = 155.5 + 49.5(M-1) + M \cdot N \cdot 4.55$ µs

> Minimum t_{d1} (µs) 155.53 +1·N·4.55 **X=1** 205.03 +2·N·4.55 M=2 M=3 254.53 +3·N·4.55 M=4304.03 +4·N·4.55 M=5 353.53 +5·N·4.55 403.03 +6·N·4.55 **M=6** X=7 452.53 +7·N·4.55 M=8 502.03 +8·N·4.55 M=9 551.53 +9·N·4.55 M=10 601.03 +10·N·4.55

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Minimum t_{d1} (µs) for 0.01 N KC1 solution (SWDLY1 = 2, SWDLY2 = 3).

M=1:	169.18	X=2 :	232.33	M=3:	295.48
M=4 :	358.63	M=5:	421.78	M=6:	484.93
X=7:	548.48	X=8:	611.23	X=9:	674.38
M=10:	737.53	N=11:	800.68	M=12:	863.83
M=13:	926.98	X=14:	990.13		

APPENDIX B

Programs for Data Acquisition and Data Analysis

The names and the functions of the programs for data acquisition and data analysis are listed below.

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Table	B-1.	The	names	and	functions	of	the	data

acquisition programs

Namo	Function
CAPSEQ	Controlls the sequential operations for capacitance measurements
TIN12	Sets up timer for capacitance measurements
BLVC	Calibrates DAC baseline values
CAP	Neasures capacitances
TICP	Sets up timer for chronopotentiometric experiment
ICAPN	Neasures integral capacitive charges
CPSEQB	Controlls the sequential operations for chronopotentiometric experiment
CPADCN	Neasures electrode potential and stores it in memory
CHGADN	Injects a charge pulse or charge pulses to the electrode
VBACK	Bring the potential back to the initial value

Table B-2. The names and functions of the data analysis programs

Name	Functions			
CAP.FTN	 Divides one binary file by another for capacitance measurements; one data set is from the results of an electrochemical experiment, the other is from the results of a dummy cell 			
	2. generates a C. G. Enke β Co. standard data file and a parameter file			
CONCPAD.FTN	 Converts ADC values obtained in a chronopotentiometric experiment to a C. G. Enke β Co. standard data file and a parameter file 			

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SUBR CAPSEQ ; SAVE REGS CAPSEQ: PUSH н PSW PUSH i CAPSOO: LXI H, VINIT j ; APPROACH THE INITIAL CALL VAPR > VOLTAGE LDA VAPFLO ; GET THE FLAG CPI ; GET THE VOLTAGE? 0 JNZ CAPSQO ; NO, DO IT AGAIN CALL READY 1 CAPSQ1: LXI H, VINIT i CALL VAPR 3 LDA EFLAGS 1 RRC 1 JNC CAPSQ1 KEY BOARD IS NOT TYPED LDA BUFO ; GET THE CHARACTER CPI CTRZ ; CONTROL Z CAPSQ7 **JEXIT** JZ CPI ; QO **'**g JNZ CAPSQ1 ; NO CAP CALL 1 LXI H, CAPSMO 1 CALL PRINT ; CLEAR GRAPHICS? ; DELAY FOR 3 SEC. MVI A, 30. CAPSQ2: LXI H, 10000. 1 DELAY CALL ï DCR A 1 CAPSG2 JNZ ; LDA EFLAGS RRC CAPSG3 KEYBOARD IS NOT TYPED JNC LDA BUFO ; GET THE CHARACTER CPI **′**b 3 CZ BLANK ; CLEAR SCREEN CPI 'u 1 CZ CLEAR ; CLEAR SCREEN CZ BLANK ; CLEAR THE SCREEN MVI A, EFUO . CALL EFCLR ; CLEAR KB . LXI H, 10000. ; DELAY ; FOR . 1 SEC. CALL DELAY CAPSG3: CALL CAPGR ; DRAW THE RESULTS CALL CAPUL ; SEND DATA TO 11 JMP CAPSQO ; LOOP AROUND CAPSQ7: MVI A, EFUO CLEAR KB EVENT FLAG CALL EFCLR . POP PSW ; RESTORE REGS **202** н 1 RET ; CAPSMO: . ASCIZ /CLEAR?/ . ; TIMING PROGRAM FOR THE CAPACITANCE MEASUREMENT

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CUNNEC	I GAIES	1 AND 2 TU +5 V	
	SUBR	TIM12	
TIM12:	PUSH	н	; SAVE REGS
	PUSH	D	3
	PUSH	PSW	i
	CALL	TIRSET	RESET TIMER
	MVI	A, 1	COUNTER #1 MODE REG
	STA	TICMD	3
	LXI	H, TIDATA	GET ADDR OF TIMER DATA
			PORT
	MUT	M. 249	I ENABLE SPECIAL GATE
			FROM LOAD PEO, PEPETITIVELV
			ATN DOWN ACTIVE HIGH
	M4 4 T	×	ACTIVE VIOL LEVEL OATE N EI
		n, 233	JACIIVE HIGH LEVEL GAIE NIFI
	XCHG		
	LHLD	TIPD	GET TIMER DELAY
	XCHG		<i>i</i>
	MOV	M, E	TO LOAD REG
	MOV	M, D	i
	MOV	M, E	; TO HOLD REG
	MOV	M, D	i
	MVI	M, 202	COUNTER #2 MODE REG
			ENABLE SPECIAL GATE
			FROM LOAD REQ, ONCE
			BIN, DOWN, ACTIVE HIGH
			TOGGLE, DELAYED
	MVI	M. 220	ACTIVE HIGH LEVEL GATE N
			: TCN-1
	YCHO		
		TINN	A OFT TIMER DELAV
			DECREMENT TO MATCH
		n	$\frac{1}{2} \frac{1}{2} \frac{1}$
	VCUO		JDELAT - HIFD + HIDD
		M F	
	MUV		JIU LUAD REG
	MUV		3
	MUV	M, E	;
	MOV	M, D	;
	POP	PSW	RESTORE REGS
	POP	D	3
	POP	Η.	3
	RET		AND GO HOME
i			•
J			
THIS R	DUTINE D	ETERMINES THE DAC	VALUE
FOR AD	DING ZER	DCHARGE	
	SUBR	BLVC	
BLVC:	PUSH	H	SAVE REGISTERS
	PUSH	D	
	PUSH	B	• \$
	PUSH	- 991	r 1
			, INTRUTE TO TERMINAL ONLY
			COMDADE UINIT AUETNAL
	VALL	DLVVI	I A A A A A A A A A A A A A A A A A A A

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	JM	BLVC5	; VFINAL IS MORE POSITIVE
	JNZ	BLVC11	VFINAL IS MORE NEGATIVE
BLVC4:	MVI	A, 1	STORE THE RATIO
	STA	BLDAR1	i
	MVI	A, O	i
	STA	BL DAR2	
	JMP	BL VC47	
· 057 TO	THE NEE		CHARGE AND CHECK
TUE DI	PECTION (
DIUC1.			
BLVCI.		n' BEAUI	
		A 100	I ACA TIMER
DL UCO.			ADDDDACH THE DECIDED
BLVCZ:	CALL	VAPR	JAPPRUACH THE DESIRED
			
	CALL	ADCODE	JGET ADC VALUE IN DE
	CALL	ADCBDP	PRINT THE VALUE
	CALL	CRLF	CARRIAGE RETURN, LINE FEED
	DCR	A	;
	JNZ	BLVC2	LOOP ARROUND TILL DONE
	MVI	A, 377	;255 TIMES AGAIN
BLVC3:	CALL	VAPR	1
	DCR	Α	3
	JNZ	BLVC3	1
	CALL	ADC6DE	GET ADC VALUE INTO DE
	XCHG		;GET INTO HL
	SHLD	BLVCV1	STORE IT
	LHLD	BLDA1	DAC VALUE FOR ADDING
			CHARGE
	XCHG		INTO DE
	LDA	BLSW	; 54 #
	MOV	B. A	
		BL NUM1	NUMBER OF CHARGE ADDITIONS
	MOV	C. A	
	CALL	CGADDC	ADD CHARGE HI =DAC VALUE
		0011220	
	CALL	ADCADE	GET ADC VALUE IN DE
		BLUCU1	ACT VINIT
	YCHO		(DE)-UINIT (UL)-UEINAL
		u	
			/ COINT LINIT
		ADCRED	FRINT VINIT
	CALL		i
	CALL		i
			;
	CALL	PRINI	1
	909	н	
	XCHG		;(DE)=VFINAL (HL)=VINIT
	CALL	ADCBDP	j
	CALL	CRLF	1
	CALL	DCMP	; (DE)-(HL) FLAGS BASED ON
			; RESULTS
	RET		; AND GO HOME
: VETNAL	IS MOPE	PORTTIVE	

	BLVC5:	CALL	DSUB	; (HL)=(HL)-(DE)
		MOV	A, L	;
		STA	BLDAR1	STORE THE RATIO FOR MORE
		LXI	H, BLDA1	; GET ADDRESS OF DAC VALUE
		CALL	DCXM	; INCREASE MEMORY WORD,
				DECREASE DAC VALUE
		CALL	BLVC1	;DD IT AGAIN
		JM	BLVC5	STILL MORE POSITIVE
		JZ	BLVC4	>VFINAL=VINIT
		CALL	DSUB	; HL=HL-DE
		CALL	HLTCMP	; TWO'S COMPLEMENT OF HL
		MOV	A, L	i
		STA	BLDAR2	STORE THE RATIO FOR MORE NEGATIVE V
		JMP	BLVC47	; FINISH
	; VFINAL	IS MORE	NEGATIVE	
	BLVC11:	CALL	DSUB	; HL=HL-DE
		CALL	HLTCMP	; TWO'S COMPLEMENT OF HL
		MOV	A, L	3
		STA	BLDAR2	STORE THE RATIO FOR
				MORE NEGATIVE V
		LXI	H, BLDA1	; GET ADDRESS OF DAC VALUE
		CALL	INXM	; DECREASE MEMORY WORD,
				; INCREASE DAC VOLTAGE
		CALL	BLVC1	; DO IT AGAIN
		JZ	BLVC4	; VFINAL=VINIT
		JP	BLVC11	STILL MORE NEGATIVE
		CALL	DSUB	; HL=HL-DE
		MOV	A, L	3
		STA	BLDAR1	STORE THE RATIO FOR
				MORE POSITIVE V
		LXI	H, BLDA1	; ADJUST DAC TO BE
				MORE POSITIVE
		CALL	DCXM	J
	BLVC47:	POP	PSW	RESTORE REGISTERS
		POP	B	j
		POP	D	3
		POP	н	j
		RET		; AND GO HOME
	BLFLG:	.BLKB 1		; 1: VFINAL+, 0: VFINAL-
	BLVCV1:	.BLKB 2		; TEMP VOLTAGE STORAGE
	3			
	;			
	; BLDA1:	BASELINE	E VOLTAGE FOR DAC	
) MO:	MINIMUM	DATA POINTS FOR	EACH POTENTIAL STEP
	; NO:	SHORTER	# OF LOOPS FOR K	EEPING POTENTIAL
	; N1:	LONGER 4	OF LOOPS FOR LE	EPING POTENTIAL
	/ N2:	DAC INCR	REMENT FOR CAPACI	TANCE MEASUREMENT
	; SWA:	SWITCH #	FOR ADDING CHAR	GE, USE #4
	; SWDLY1:	DELAY FO	DR CHARGING CAPAC	ITOR,
	;	USE 10	IS ENDUGH FOR . 1	UF CAP
	VSTPO:	VOLTAGE	STEP FOR CAPACIT	ANCE MEASUREMENT

VSTP1: VOLTAGE STEP FOR POTENTIAL SCANNING 1 ; 2 ; MTMP: VOLTAGE APPROXIMATION FLAG 1=>ADJUSTED OTHERS => NOT ADJUSTED MTMP1: TEMPORARY CHG QUANTITY MXTMP: POTENTIAL BEFORE CHG ADD MXTMP1: DAC VALUE FOR CHARGE ADDITION IMXTMP2: ADDR FOR STORING SUMMATION OF CHARGE MXTMP3: ADDR FOR STORING # OF DATA POINTS FOR THE POTENTIAL ; DELAY = TIPD + TIDD ; TIPD >= 32, TIDD >= 4 SUBR CAP ; SAVE REQS CAP: PUSH н PUSH D 1 PUSH B 3 PUSH PSW 3 LHLD VINIT ; INITIAL VOLTAGE SHLD MXTMP 1 STARTING ADDR FOR STORING H, ADCLIB LXI SUMMATION OF CHG SHLD MXTMP2 . LXI D, CAPEND ; ENDING ADDR CAP1: MVI M, O CLEAR MEM INX JINCREASE MEM ADDR н CALL DCMP ; DE-HL CAP1 JNZ . LXI H, 1 1 CLEAR # OF DATA POINTS SHLD DATAN LXI H, 0 3 CLEAR HIGH WORD FOR ADD SHLD MX2TM5+2 MVI A, 0 3 STA MTMP CLEAR VOLTAGE ; APPROXIMATION FLAG ;1=> ADJUSTED ; O=> NOT ADJUSTED CALL SET UP COUNTER #1 & #2 TIM12 MVI A, 141 1 STA TICMD ; LOAD AND ARM COUNTER #1 LXI H, TCLIB STARTING ADDR FOR STORING ; # OF DATA POINTS FOR JEACH VOLTAGE SHLD MXTMP3 ; LXI D, TCLIB+4000 ; CAP2: MVI M, O ; CLEAR MEM INX ; INCREASE MEM ADDR H CALL DCMP ; DE-HL JNZ CAP2 ; CAP3: LXI H, MXTMP CALL KEEP AT THE POTENTIAL VAPN1 ; FOR A WHILE(LONGER) LHLD DAO ; GET DAC VALUE

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	XCHG		; INTO DE
	CALL	DACSUP	SET UP DAC AND CHG CAP
	LDA	TICMD	GET TIMER STATUS
	ANI	2	; DUT1
	JNZ	CAP3B	; 1, CHECK FOR $1 \rightarrow 0$
CAP3A:	LDA	TICMD	
	ANI	2	· · ·
	J7	CAP3A	2
	JMP	CAPBC	· 0 -> 1
CAP3B	I DA	TICMD	$: CHECK FOR 1 \rightarrow 0$
•••• •••	ANT	2	
	. 1N 7		· · · · · · · · · · · · · · · · · · ·
CAPBO	MUT	A. 352	1
	STA	TICMD	, 9 SET 01170
	CTA	TICMD	LUND AND ART COUNTER #2
	JIM		
	CDA CTA	SWA CU	
CADA.	514		JEND THE CHU TO ELECTRODE
CAF4:		TICHD	
	ANI	4	COUNTER #2 STATUS BIT
	JNZ	CAP4	INUT YET, WAIT
	CALL	ADC6DE	MEASURE THE PUTENTIAL
	LHLD	MXTMP	GET POTENTIAL BEFORE
			CHARGE ADDITION
	XCHQ		;
	CALL	DSUB	GET VOLTAGE DIFFERENCE
	LXI	D, O	j
	CALL	DCMP	i
	JM	CAP6	; O. K.
CHG NOT	r enough,	ADD MORE	
	LHLD	DAO	GET DAC VALUE FOR CAP
	XCHG		INTO DE
	LHLD	BLDA1	; GET BASELINE DAC VALUE
	CALL	DSUB	; HL-DE
	XCHG		3
	MVI	A, 2	i
	CALL	DMULT	; DE=DE+A
	LHLD	BLDA1	; GET BASELINE DAC VALUE
	CALL	DSUB	CALC NEW DAC FOR CAP
	SHLD	DAO	STORE IT
	JMP	САРЭ	; ADD CHC AGAIN
3			
CAP6:	SHLD	MXTMP4	STORE POTENTIAL DIFFERENCE
	LDA	VSTPO	VOLTAGE STEP FOR CAP
	DCR	A	START FROM 3LSB LESS
	DCR	A	3
	DCR	Α	i
	CMP	L	3
	JZ	CAP20	PROPER VOLTAGE TO START
	JP	CAP15	VOLTAGE TOO LITTLE
			CHECK APPROXIMATION FLAG
VOLTAGE	E TOO MUC	CH, CALC PROPER C	HARGE
CAP10:	MVI	A, 1	j

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	STA	MTMP	SET APPROXIMATION FLAG
	LHLD	DAO	GET DAC VALUE FOR CAP
	XCHC		INTO DE
	LHLD	BI DA1	GET BASE LINE DAC VALUE
	CALL	DSUB	
	YCHO	2002	
		USTRO	, DERIBED VOLTAGE RTEP
	CDW	VAIPO	
	DCD	•	DECOEMENT BY 3
	DCR	A	
	DCR	A	2
	DCR	A	
	CALL	DMULT	
	LDA	MXTMP4	GET ACTUAL VULTAGE STEP
	CALL	DIV	; DE=DE/A
	LHLD	BLDA1	GET BASELINE DAC VALUE
	CALL	DSUB	CALC THE DAC VALUE FOR CAP
	SHLD	DAO	i
	JMP	CAP3	TRY TO APPROXIMATE THE CHG
			; ADD AGAIN
; VOLTAC	E TOO LI.	TTLE, CHECK APPRO	XIMATION FLAG,
; IF APP	ROXIMATE	D, GO AHEAD AND D	O IT
CAP15:	LDA	MTMP	GET APPROXIMATION FLAG
	CPI	1	APPROXIMATED?
	JN7	CAPIO	INGT VET. APPROXIMATE IT
CAP20.	MUT	A. O	
	CTA		CLEAD ADDONIMATION ELAC
CTARTI	JIA No addini	THRE THOSEMENTING CH	A TH ORDER TO MEACURE CAR
791MK11	NG ADDINI	S INCREMENTING CH	OFT DAG MALUE
CAP21:		DAU	IGEI DAL VALUE
	SHLD		
CAP22:	LHLD	MXIMP1	IGET DAG VALUE FUR CHG ADD
	XCHG		INTO DE
	LHLD	BLDA1	GET DAC BASELINE VULTAGE
	CALL	DSUB	; HL-DE
	XCHG		RESULTS INTO DE
	LHLD	BLDA1	; GET BASELINE DAC VALUE
	DAD	D	; CALC DAC VALUE FOR
			; REVERSE CHARGE
	CALL	CGADD	; ADD THE CHG TO ELECTRODE
	LDA	SWDLY3	; GET SWITCH DELAY
	CPI	0	IND DELAY?
	JZ	CAP22D	; YES
	CALL	VAPDEL	DELAY FOR A WHILE
CAP22D:	LXI	H. MXTMP	GET POTENTIAL BEFORE
			CHARGE ADDITION
	CALL	VAPNO	APPROACH THE POTENTIAL
		MYTMD 1	
		CIA I FIF 4	INTO DE
		DACCUR	
	UALL		JOEI UT DALI UTU UAT
	LUA	TICMD	IVEL LIMER STATUS
	ANI	2	
	JNZ	CAP22B	;1, CHECK FOR 1-> 0

CAP22A:	LDA	TICMD	i
	ANI	2	;
	JZ	CAP22A	i
	JMP	CAP22C	$(0 \rightarrow 1)$
CAP22R.		TICMD	CHECK FOR 1 -> 0
		7	
			i
CAP22C:	MV1	A, 352)
	STA	TICMD	SET OUT2
	MVI	A, 142	3
	STA	TICMD	LOAD AND ARM COUNTER #2
	LDA	SWA	3
	STA	SW	ADD CHQ TO ELECTRODE
CAP24:	LDA	TICMD	CHECK COUNTER #2
	ANT	A	COUNTER #2 STATUS BIT
	. 1617	CAPOA	NOT VET. MAIT
			ACT THE BOTENTIAL
	CALL	ADCODE	
	LHLD	MX IMP	GET PUTENTIAL BEFURE
			CHARGE ADDITION
	XCHG		5
	CALL	DSUB	POTENTIAL AFTER CHQ ADD -
			POTENTIAL BEFORE CHQ ADD
	LDA	VSTPO	VOLTAGE STEP FOR CAP
	SUB	L	1
	JNZ	CAPBO	VOI TAGE NOT EQUAL DESTRED
		DERIDED HALLE	
		DESIRED VALUE	
CAP24A:			VEI DAC VALUE FUR CAP
	XCHG		INTO DE
	LHLD	BLDA1	GET BASELINE DAC VALUE
	CALL	DSUB	; HL-DE
	SHLD	MX2TM5	3
	LHLD	MXTMP2	GET ADDR FOR CHG SUMMATION
	LXI	D, MX2TM4	;
	MVI	Δ, 4	i
	CALL	MTRE	TRANSEER PREVIOUSLY
			· INTO MYOTMA
	CAL 1	DDADD	/ 1810 PIACTUT • / 6 \
			(0) = (4) + (3)
	LHLD	MX I MP2	GET ADDR UP SUM UP CHG
	XCHG		FOR DESTINATION
	LXI	H, MX2TM6	GET ADDR OF SOURCE
	CALL	MTRF	; TRANSFER 4 BYTES OF MEM
	LHLD	MXTMP3	; GET ADDR OF COUNTER OF
			;# OF CHG ADD
	CALL	INXM	INCREMENT THE COUNT
	LHLD	MXTMP1	GET DAC VALUE
	LDA	N2	
	MOV	E.A	•
	MUT		<i>.</i>
	1 100		
	CALL		i ML-DE
	LXI	D, O	; ;
	CALL	DCMP	; DE-HL

	JZ	CAP25	MOST NEGATIVE VOLTAGE
			; ALLOWED
	JP	CAP26	OUT OF BOUNDARY
CAP25:	SHLD	MXTMP1	STORE DAC VALUE FOR NEXT
			CHG ADD
	JMP	CAP22	; ADD MORE CHG TO GET CAP
CAP26:	CALL	ERROR	SEND ERROR MESSAGE
	JMP	CAP41	; GO TO NEXT VLOTAGE STEP
VOLTAGE	E NOT WIT	THIN TO THE DESIR	ED VALUE +- 1LSB
CAP30:	CPI	1	3
	JZ	CAP24A	;
	CPI	-1	;
	JZ	CAP24A	3
	CPI	-2	3
	JM	CAP40	VOLTAGE HIGH ENDUGH
	LHED	MXTMP1	ACT DAC VALUE
		N2	AFT DAC STEP
	MOV	F.A	
	MUT	D. 0	2
	CALL	DELIB	CALC NEXT DAC VALUE
		D. 0	MORT NEGATIVE DAC VALUE
			DE-LU
	.17		
			· EDDOG ON TO NEVI STED
CAP31 ·			ATOR DAC UNITE END NEXT
	JALD	FIA I FIF 1	
	IMD	CAR00	AND MODE CHA TO AET CAR
UNI TAOS	T UTOU EN	VARZE Jana Cueck 4 ae	TADD MURE CHO IL VEI CHP
	LEYT UNI	TAGE STEP	
		MYTMP3	ACT ADDE EOR STORING
UNF TV .		PIA THE 3	HADDE FOR STORING
	MOV	F.M	A OF DATA POINTS INTO DE
	TNY	с/п ц	
	MOU	D. M	, ,
		MO	ACT MINIMUM DATA DOINTO
		HO	
	C AL 1	DOMB	
			NOT CHOUCH DATA BOINTS
	UN	CALET	ACT THE STARTING BOTENTIAL
			AUTORI V AND DO IT MORE
. # OF D			THE HOLTAGE OF INTEREST
	NEXT UNIT		THE VULIAWE OF INTEREST
CAPA1 -		NYTMD	
UNC 71.			
	I DA	URTRI	UNITAGE STEP END SCANNING
		A2161	CALC UNITAGE END NEXT STEP
	VCUA		ACCINE TO INTO DE
			ACT CINAL UN TAAC
		VE TIMUE	NET LINUE AAFLAGE
		UNF DU	NEXT UN TAGE INTO HI
		MYTMO	ATORE IT
	LHLD	ua i an	JUEL TO VUL (AGE SIEM

INX H ; INCREMENT ONE SHLD DATAN STORE IT LHLD MXTMP3 ; GET ADDR FOR STORING # OF DATA POINTS IN EACH STEP INX Н ; INCREMENT BY 2 INX H 1 SHLD MXTMP3 ; STORE IT LHLD GET ADDR FOR STORING SUM MXTMP2 ; OF CHQ FOR THE VOLTAGE ; STEP INX ; INCREMENT BY 4 H INX Н 1 INX H 1 INX Н 2 SHLD MXTMP2 STORE IT MVI A, 0 1 CLEAR VOLTAGE STA MTMP APPROXIMATION FLAG ; OD TO NEXT VOLTAGE STEP JMP CAP3 CAP50: POP PSW RESTORE REGS POP B 1 909 D 3 , POP H 1 RET ; AND GO HOME 3 ; TIMER FOR CHRONOPOTENTIOMETRY, CONNECT OUT2 TO SRC2, ; GATES 1, 2, AND 4 TO +5V ; OUT4 TO GATE 3, -OUT4 TO IR3, OUT3 TO IR4 ; IR3 AND IR4 ARE INPUTS OF THE INTERRUPT CONTROLLER OF 8259 SUBR TICP TICP: PUSH H ; SAVE REQS PUSH D 3 PUSH PSW 1 CALL TIRSET ; RESET TIMER ICOUNTER 2 MODE REG MVI A, 2 STA TICMD 1 LXI H, TIDATA ; GET ADDR OF TIMER DATA PORT MVI M, 242 ; ENABLE SPECIAL GATE, ; FROM LOAD, REPETITIVELY, ; BIN, DOWN, ACTIVE HIGH ; TOGGLE, DELAYED ACTIVE HIGH LEVE GATE N. F1 MUI M, 233 XCHG SAVE ADDR OF TIDATA LHLD TIDD ; DATA DELAY XCHG 3 MOV M,E TO LOAD REG MOV M, D . MOV M, E TO HOLD REG MOV M, D 1 MVI M, 242 ; COUNTER 3 MODE REG ; SAME AS COUNTER 2

			GATE IS CONTROLLED BY OUT4
	MVI	M, 233	;
	MOV	M, E	; DATA DELAY TO
	MOV	M, D	; LOAD REG
	MOV	M, E	; TO HOLD REG
	MOV	M, D	1
	MVI	M, 302	COUNTER 4 MODE REQ
			;ENABLE SPECIAL GATE
		٠	; RELOAD FROM LOAD OR HOLD
			; COUNT ONCE, BIN, DOWN, ACTIVE
			HIGH TOGGLE, DELAYED
	MVI	M, 202	SRC2(CONNECT OUT2 TO SRC2)
			; RISING EDGE,
			ACTIVE HIGH GATE N
	XCHQ		SAVE ADDR OF TIDATA
	LHLD	TIPD	PRE EXP DELAY
	XCHQ		3
	MOV	M, E	; TO HOLD REG, FROM TIPD
	MOV	M, D	3
	MVI	M, O	;512 DATA POINTS, LOAD REG
	MVI	M, 2	3
	MVI	A, 156	1
	STA	TICMD	; LOAD AND ARM COUNTER 4, 3, 2
	POP	PSW	1
	POP	D	3
	909	н	<i>i</i>
	RET		AND GO HOME
3			
; ;			
I HIS R	UUTINE A	DDS CHARGE IU IME	ELECTRUDE AND CALCULATE
JITE IN	IEGRAL U	AFACITIVE CHARGE	FUR EACH VULIAGE
	THE SE	R FUR THE VULTAGE	
	· CHARCE	COND VOLINGE DIFF	ADDITION
· MYTMDA	· ADDEC	E EOD INTEODAL CA	ADDITION CHADAE
	· ADDRES	B FOR INTEGRAL CA	NACI IVE CHARGE
		TCADN	
ICAPN.	PUSH		SAVE REGISTERS
4 UFW (4.	PUSH	D	
	PUSH	8	
	PUSH	PSH	
	LXI	H, O	CLEAR MEMORY
	SHLD	BICAP	
	SHLD	BICAP+2	3
	LXI	H, 1	SET UP COUNTER FOR VOLTAGE
	SHLD	MXTMP1	;
	LDA	ICAPC1	GET NUMBER OF CHARGE
			ADDITIONS
	LXI	H, ICAPDA	;
	CALL	SMDAC	SET DAC PARAMETERS
	MVI	A, 1	; RESET
	STA	ICAPC2	; # OF CHARGE ADDITION LOOP
ICPNOA:	LXI	H, VINIT	; APPROACH THE VOLTAGE

.

	CALL	UARNO	
	CALL		
TORNO.	LALL		ARROADU TUS UN TAAS
ICPNO:		H, VINI I	APPRUACH THE VULTAGE
	CALL	VAPR	
	CALL	CHARIN	IS KEYBUARD TYPED?
	JNC	ICPNO	;NO, WAIT
	CPI	2	÷ 00?
	JNZ	ICPNO	; NO, WAIT
ICPN1:	LXI	H, VINIT	j
	CALL	VAPNO	; APPROACH THE VOLTAGE
	LHLD	ADCTMP	; GET THE VOLTAGE
	XCHQ		; (DE) = TEMP VOLTAGE
ICPN2:	LXI	H, VINIT	; APPROACH THE VOLTAGE
	CALL	VAPN1	;
	CALL	ADC6DE	JOET THE DAC VALUE
	LHLD	VINIT	GET INIT VOLTAGE
	XCHG		(HL)=DAC VALUE
	CALL	DCMP	(VINIT)-ADC VALUE
	JNZ	ICPN2	ADC VALUE IS NOT EQUAL
			TO VINIT. DO IT AGAIN
	SHUD	ADCIMP	STOPE BEGINING POTENTIAL
	I DA	TCAPCO	AFT NUMBER OF CHARGE 1 OOP
TCON2.			
ICFNG.		TCADOLI	ART OU A
		ICHLOM D V	
			JINIU B
	LDA	ICAPDA	JGET DAG LUW BYTE
	STA	DACLB	
	LDA	ICAPDA+1	; GET DAC HIGH BYTE
	STA	DACHB	j
	POP	PSW	; GET # OF CHARGE LOOP
ICPN3A:	PUSH	PSW	; SAVE # OF CHQ LOOP LEFT
	LDA	ICAPC1	; Get # OF SMALL CHG LOOP
	MOV	C, A	; SAVE IT
ICPN3B:	MVI	A, 1	i
	STA	SW	CHARGE UP CAPACITOR
	LDA	SWDLY1	GET SWITCH DELAY
ICPN3C:	DCR	A	3
	JNZ	ICPN3C	3
	MOV	A, B	GET SWITCH NUMBER
	STA	SW	;
	CALL	SWDLYB	3
	DCR	C	DECREASE SMALL CHO LOOP
	JNZ	ICPN3B	IND FINISH VET. DO IT AGAIN
	PNP	PRU	
	DCR	A	
		TCBNOA	IND DO IT COME MODE
	CALI		ACT DAC VALUE
			ACT CINAL UCITAGE
		A DO	ADD ADOUT DO -U MODE
			AND ABOVI ZU MV MUKE
	CALL	MLMA Domo	
	CALL	DCHP	; de-hl; flags based un ; results
	JP	ICPN11	; VOLTAGE IS MORE NEGATIVE

			JOR EQUAL TO VEINAL
	CALL	ICPN4	; CALCULATE THE CAP CHARGE &
			STORE THEM
	JMP	ICPN1	DO IT SOME MORE
; CALCUL	ATE CAPA	CITIVE CHARGE, STO	RE THEM AND
INCREM	ENT NUMB	ER DE CHARGE LOOP)
TCPN4	LHED	ADCTMP	1
	YCHO		, UL - DOTENTIAL AFTED CHO ADD
			DE-DECINING BOTENTIAL
	0000		ADE-DEGINING FUIENIIME
	PUSH	F3W	J SAVE REG
	LUA	DAADBF	JGEI BIN/DEC FLAG
	CPI	2	1
	JZ	ICPN5	NO PRINTING
	CALL	ADCBDP	PRINT BEGINNING POT
	XCHQ		3
	CALL	ADCBDP	PRINT POT AFTER CHQ ADD
	XCHQ		3
	CALL	LPCRLF	CARRIAGE RETURN, LINE FEED
ICPN5:	POP	PSW	RESTORE REG
	CALL	DSUB	; HL=HL-DE
	JM	ICPN10	NOT ENDUGH CHARGE
	SHLD	MXTMP2	STORE 2nd VOLTAGE
ICPNA.	LHED	ICAPDA	AFT THE DAC VALUE &
201110.	test i ten def		
	CALL	COCALN	
	UALL	UUUALN	
		MYTHOO	
	SHLU	MX I MP3	CHO QUANTITY UP UNE ADD
	LUA	ICAPCZ	IGEI WUF CHARGE LUUP
	DCR	A	DECREMENT ONE CHO # FOR
			; THE FIRST COLTAGE
	MOV	C, A	3
	MVI	B, O	;
	CALL	DDMULT	; (HLBC)=(HL)+(BC)
	SHLD	MX2TM4+2	;
	MOV	L, C	;
	MOV	H, B	;
	SHLD	MX2TM4	;
	LHLD	MXTMP 1	GET THE 1ST VOLTAGE
	DCX	H	DECREMENT ONE IN ORDER
		••	TO MAKE PROPER VOI TAGE
	YCHO		
		MYTMOO	
			CALL THE DIFFERENCE
	SALD		STUKE THE DIFFERENCE
		PIX PPP 1	JUEI IST VULIAGE DIFFERENCE
	XCHG		INTO DE
	MVI	A, 4	34 BITS FOR EACH DATA POINT
	CALL	DMULT	; DE=DE*A
	LXI	H, BICAP	; GET BASE MEMORY ADDR FOR
			; ICAP
	DAD	D	; (HL)<-(HL)+(DE)
	SHLD	MXTMP4	STORE ADDR FOR INT CAP CHG

	MVI	C, 1	; COUNTER
	MVI	B, O	i
ICPN61:	LHLD	MXTMP3	GET CHO OF ONE ADD
	PUSH	B	STORE COUNTER COUNTER
	CALL	DDMULT	; (HLBC)=(HL)*(BC)
	PUSH	н	
	IHLD	MYTMP5	AFT VOI TAGE DIFFERENCE
	XCHG		
		Ц	
	CALL		/ //// PC/-/// PC///DE/
			· (ALDC)=(ALDC)/(DE)
		MAZ 1 MOTZ	/ ////////////////////////////////////
			; (HL) <=>(BC)
	SHLU	mx21m5	3
	PUP	8	;
	INR	C	INCREMENT COUNTER
	CALL	DDADD	;(4) + (5) => (6)
	LHLD	MXTMP4	JGET. ADDR FOR INT CAP CHG
	MOV	E, L	; INTO DE
	MOV	D, H	3
	MVI	A, 4	34 BYTES EACH DATA POINT
	CALL	HLPA	CALC NEXT ADDR
	SHLD	MXTMP4	STORE NEW CAP CHQ ADDR
	LXI	H, MX2TM6	3
	CALL	MTRF	TRANSFER RESULTS TO MEM
	LXI	D, MXTMP1	;
	LXI	H, MXTMP2	3
	CALL	DCMPM	; ((DE))-((HL))
	JP	ICPN7	1 ST VINTAGE FOUAL TO OR
			CREATER THAN 2nd VOL TAGE
ICPNA2.	XCHQ		
			COUNTED
	CALL	TNYM	TNODEMENT ONE
		TCPN41	CALCULATE TILL ITS DONE
TCPN7.		10-1401	ACT ADDR OF HOLTAGE
ICFN7.			A OUNTED
	0.41.1	TNIMM	
	LALL		JINCREMENT UNE
		H, ICAPCZ	
	INK	n	INCREMENT CHARGE LUUP
	RE1		; AND GO HOME
INUT EN	DUCH CHAI	RGE	
ICPN10:	909	PSW	; REMOVE JUNK
	LXI	H, ICAPC2	; GET ADDR OF # OF CHARGE
			; LOOP
	INR	M	; INCREASE IT
	JMP	ICPN1	; PROCEED THE EXP
ICPN11:	LDA	DAADBF	; GET BIN/DEC FLAG
	PUSH	PSW	; SAVE IT
	MVI	A, 0	3
	STA	DAADBF	MAKE IT PRINT ADC VALUES
	CALL	ICPN4	PRINT VOLTAGES
	POP	PSW	RESTORE ORIGINAL BIN/DEC
	STA	DAADBF	;FLAQ
			AND STORE THEM

LXI H, PICAP2 ; PRINT # OF LOOP CALL SPRMPR : POP PSW ; RESTORE REGISTERS POP B 1 POP D 1 Н POP 1 RET ; AND GO HOME ; ; ; SUBR CPSEQB CPSEGB: PUSH H ; SAVE REQS PUSH PSW 1 LXI H, CPSEBM ; NITIALIZE? CALL PRINT 1 CPSEB1: CALL CHARIN ; JNC CPSEB1 1 CPI 4 U ; YES JNZ CPSEB2 ; NO CALL CPSDLB SET CP INITIAL LIB CPSEB2: LXI H, CPSEM2 ; CALL PRINT ; EXIT? CALL DELAY2 ; DELAY FOR 2 SEC LDA EFLAGS ; GET EVENT FLAG RRC ; CARRY KEY BOARD IS TYPED JNC CPSEB4 ; NO ; GET THE CHARACTER LDA BUFO CPI 'u ; EXIT? JZ CPSEB7 ; YES CPSEB4: CALL CPB CP & VOLTAGE BACK LXI H, CPSEM1 1 CALL PRINT ; CLEAR GRAPHICS? CALL DELAY2 ; DELAY FOR 2 SEC. LDA EFLAGS ; GET EVENT FLAGS RRC . KEYBOARD IS NOT TYPED JNC CPSB4A LDA BUFO ; GET THE CHARACTER CPI **1**b 3 CZ BLANK ; CLEAR VRAM CPI 'u ; CZ CLEAR ; CLEAR GRAPHICS CZ BLANK ; CLEAR VRAM A, EFUO MVI ; CALL EFCLR ; CLEAR KB CALL DELYP1 ; DELAY FOR . 1 SEC CPSB4A: CALL ADOR ; DRAW THE VOLTAGE GRAPH CALL CPADUL ; SEND ADC VALUES CALL CRLF ; CARRIAGE RETURN, LINE FEED LXI H, PCPC02 CORRECTION RATIO APARAM SHLD ; ADDR FOR THE PARM CALL PARMPR PRINT THE PARAMETER CALL TTYINB ; INPUT A LINE, BREAK DOWN CALL PARMCH ; CHANGE THE PARAMETER
	CALL	VBACK	KEEP VINIT FOR A WHILE
	LXI	H, PAVEW	AVERAGE WIDTH
	SHLD	APARAM	ADDR OF THE PARM
	CALL	PARMPR	PRINT THE PARAMETER
	CALL	TTYINB	I TNPLIT A LINE. BREAK DOWN
	CALL	PARMCU	CUANCE THE DADAMETER
			\cdot
		VBACA U COCOMI	ACT VINII FUR A WHILE
			PRINT IGAEN?
	CALL	PRINI	3
CPSEB5:	CALL	CHARIN	3
	JNC	CPSEB5	;
	CPI	í y	; YES
	CZ	ICAPN	; GET NEW CAPACITIVE CHG
	CALL	CPFCUL	SEND FC VALUES
	CALL	CPTCNX	; EXTRAPLOATE TC
	CALL	CPTCSM	SMOOTH CHARGE JUMP
	CALL	VBACK	KEEP VINIT FOR A WHILE
	CALL	CPTCCF	CLEAR FLAGS
	CALL	CPTCAV	AVERAGE TC
	CALL	CPDTC	CHOP DEF TOUTR AND
		01210	ETLI DAC LTR
	IMC	C86583	
CO0587.		ocu	
CFSED7:	FUF		IREDIURE REWO
		A	3
	REI		3
COEEDM.	ASCI7	/CPGN 87/	
Graebri.			
CPSBM1:	. ASCIZ	/ICAPN?/	
CPSBM1:	. ASCIZ	/ICAPN?/	
CPSBM1:	ASCIZ	/ICAPN?/ AKES THE ADC VALU	ES FOR CHRONOPOT AND STORE
CPSBM1: ; ; THIS R ; IT IN	ASCIZ	/ICAPN?/ AKES THE ADC VALU EFERED BY DE	ES FOR CHRONOPOT AND STORE
CPSBM1: ; ; THIS R ; IT IN (ASCIZ	/ICAPN?/ AKES THE ADC VALU EFERED BY DE CPADCN	ES FOR CHRONOPOT AND STORE
CPSBM1: ; ; THIS R ; IT IN (CPADCN:	ASCIZ DUTINE TA MEMORY RI SUBR STA	/ICAPN?/ AKES THE ADC VALU EFERED BY DE CPADCN ADCST	ES FOR CHRONOPOT AND STORE
CPSBM1: ; THIS R ; IT IN (CPADCN:	ASCIZ	/ICAPN?/ AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1	ES FOR CHRONOPOT AND STORE ;STARTS ADC ;GET FINAL VOLT HIGH BYTE
CPSBM1: ; ; THIS R ; IT IN (CPADCN:	ASCIZ DUTINE TO MEMORY RI SUBR STA LDA MOV	/ICAPN?/ AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C, A	ES FOR CHRONOPOT AND STORE ;STARTS ADC ;GET FINAL VOLT HIGH BYTE ;STORE IT
CPSBM1: ; ; THIS R ; IT IN (CPADCN:	ASCIZ DUTINE TO MEMORY RI SUBR STA LDA MOV NOP	/ICAPN?/ AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C, A	ES FOR CHRONOPOT AND STORE STARTS ADC GET FINAL VOLT HIGH BYTE STORE IT MAIT FOR ADC TO CONVERT
CPSBM1: ; ; THIS R ; IT IN (CPADCN:	ASCIZ DUTINE TO MEMORY RI SUBR STA LDA MOV NOP	/ICAPN?/ AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C, A	ES FOR CHRONOPOT AND STORE STARTS ADC GET FINAL VOLT HIGH BYTE STORE IT WAIT FOR ADC TO CONVERT
CPSBM1: ; ; THIS R ; IT IN (CPADCN:	ASCIZ	/ICAPN?/ AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C, A	ES FOR CHRONOPOT AND STORE STARTS ADC GET FINAL VOLT HIGH BYTE STORE IT WAIT FOR ADC TO CONVERT
CPSBM1: ; ; THIS R ; IT IN (CPADCN:	ASCIZ	/ICAPN?/ AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C,A	ES FOR CHRONOPOT AND STORE STARTS ADC GET FINAL VOLT HIGH BYTE STORE IT WAIT FOR ADC TO CONVERT
CPSBM1: ; ; THIS R ; IT IN (CPADCN:	ASCIZ	/ICAPN?/ AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C,A	ES FOR CHRONOPOT AND STORE STARTS ADC GET FINAL VOLT HIGH BYTE STORE IT WAIT FOR ADC TO CONVERT
CPSBM1: ; ; THIS R ; IT IN (CPADCN:	ASCIZ	AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C, A	ES FOR CHRONOPOT AND STORE STARTS ADC GET FINAL VOLT HIGH BYTE STORE IT WAIT FOR ADC TO CONVERT STORE ADC HIGH BYTE
CPSBM1: ; ; THIS R ; IT IN CPADCN:	ASCIZ	AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C,A	ES FOR CHRONOPOT AND STORE STARTS ADC GET FINAL VOLT HIGH BYTE STORE IT WAIT FOR ADC TO CONVERT S S S S S S S S S S S S S
CPSBM1: ; ; THIS R ; IT IN CPADCN:	ASCIZ OUTINE TO MEMORY RI SUBR STA LDA MOV NOP NOP NOP NOP NOP NOP NOP NOP	AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C,A ADCHB	ES FOR CHRONOPOT AND STORE STARTS ADC GET FINAL VOLT HIGH BYTE STORE IT WAIT FOR ADC TO CONVERT GET ADC HIGH BYTE MASK OUT ALL BUT ADC BITS
CPSBM1: ; ; THIS R ; IT IN CPADCN:	ASCIZ OUTINE TO MEMORY RI SUBR STA LDA MOV NOP NOP NOP NOP NOP NOP LDA ANI STAX	AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C,A ADCHB 17 D	ES FOR CHRONOPOT AND STORE STARTS ADC GET FINAL VOLT HIGH BYTE STORE IT WAIT FOR ADC TO CONVERT GET ADC HIGH BYTE MASK OUT ALL BUT ADC BITS STORE ADC HIGH BYTE
CPSBM1: ; ; THIS R ; IT IN CPADCN:	ASCIZ OUTINE TO MEMORY RI SUBR STA LDA MOV NOP NOP NOP NOP NOP NOP NOP LDA ANI STAX INX	AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C,A ADCHB 17 D	ES FOR CHRONOPOT AND STORE STARTS ADC GET FINAL VOLT HIGH BYTE STORE IT WAIT FOR ADC TO CONVERT GET ADC HIGH BYTE MASK OUT ALL BUT ADC BITS STORE ADC HIGH BYTE INC MEM ADDR
CPSBM1: ; ; THIS R ; IT IN CPADCN:	ASCIZ DUTINE TO MEMORY RI SUBR STA LDA MOV NOP NOP NOP NOP NOP NOP LDA ANI STAX INX CMP	AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C,A ADCHB 17 D C	ES FOR CHRONOPOT AND STORE STARTS ADC GET FINAL VOLT HIGH BYTE STORE IT WAIT FOR ADC TO CONVERT GET ADC HIGH BYTE MASK OUT ALL BUT ADC BITS STORE ADC HIGH BYTE INC MEM ADDR COMPARE WITH VFINAL
CPSBM1: ; ; THIS R ; IT IN CPADCN:	ASCIZ DUTINE TO MEMORY RI SUBR STA LDA MOV NOP NOP NOP NOP NOP NOP LDA ANI STAX INX CMP	AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C,A ADCHB 17 D C	ES FOR CHRONOPOT AND STORE STARTS ADC GET FINAL VOLT HIGH BYTE STORE IT WAIT FOR ADC TO CONVERT GET ADC HIGH BYTE MASK OUT ALL BUT ADC BITS STORE ADC HIGH BYTE INC MEM ADDR COMPARE WITH VFINAL HIGH BYTE
CPSBM1: ; ; THIS R(; IT IN (CPADCN:	ASCIZ DUTINE TO MEMORY RI SUBR STA LDA MOV NOP NOP NOP NOP NOP LDA ANI STAX INX CMP JZ	AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C, A ADCHB 17 D C CPADN2	ES FOR CHRONOPOT AND STORE STARTS ADC GET FINAL VOLT HIGH BYTE STORE IT WAIT FOR ADC TO CONVERT GET ADC HIGH BYTE MASK OUT ALL BUT ADC BITS STORE ADC HIGH BYTE INC MEM ADDR COMPARE WITH VFINAL HIGH BYTE HIGH BYTE EQUAL,
CPSBM1: ; ; THIS R ; IT IN (CPADCN:	ASCIZ DUTINE TO MEMORY RI SUBR STA LDA MOV NOP NOP NOP NOP NOP LDA ANI STAX INX CMP JZ	AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C.A ADCHB 17 D C C CPADN2	ES FOR CHRONOPOT AND STORE ; STARTS ADC ; GET FINAL VOLT HIGH BYTE ; STORE IT ; WAIT FOR ADC TO CONVERT ; ; ; ; ; ; ; ; ; ; ; ; ;
CPSBM1: ; ; THIS R ; IT IN (CPADCN:	ASCIZ DUTINE TA MEMORY RI SUBR STA LDA MOV NOP NOP NOP NOP NOP LDA ANI STAX INX CMP JZ JP	AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C.A ADCHB 17 D C CPADN2 CPADN3	ES FOR CHRONOPOT AND STORE ; STARTS ADC ; GET FINAL VOLT HIGH BYTE ; STORE IT ; WAIT FOR ADC TO CONVERT ; ; ; ; ; ; ; ; ; ; ; ; ;
CPSBM1: ; ; THIS R ; IT IN (CPADCN:	JZ JOUTINE TA MEMORY RI SUBR STA LDA MOV NOP NOP NOP NOP LDA ANI STAX INX CMP JZ JP LDA	AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C, A ADCHB 17 D C CPADN2 CPADN3 ADCLB	ES FOR CHRONOPOT AND STORE ; STARTS ADC ; GET FINAL VOLT HIGH BYTE ; STORE IT ; WAIT FOR ADC TO CONVERT ; ; ; ; ; ; ; ; ; ; ; ; ;
CPSBM1: ; ; THIS R ; IT IN (CPADCN:	JZ JOUTINE TA MEMORY RI SUBR STA LDA MOV NOP NOP NOP NOP LDA ANI STAX INX CMP JZ JP LDA STAX	AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C, A ADCHB 17 D C CPADN2 CPADN3 ADCLB D	ES FOR CHRONOPOT AND STORE ; STARTS ADC ; GET FINAL VOLT HIGH BYTE ; STORE IT ; WAIT FOR ADC TO CONVERT ; ; ; ; ; ; ; ; ; ; ; ; ;
CPSBM1: ; ; THIS R ; IT IN (CPADCN:	JZ JOUTINE TA MEMORY RI SUBR STA LDA MOV NOP NOP NOP NOP LDA ANI STAX INX CMP JZ JP LDA STAX INX	AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C, A ADCHB 17 D C CPADN2 CPADN3 ADCLB D	ES FOR CHRONOPOT AND STORE ; STARTS ADC ; GET FINAL VOLT HIGH BYTE ; STORE IT ; WAIT FOR ADC TO CONVERT ; ; ; ; ; ; ; ; ; ; ; ; ;
CPSBM1: ; THIS R ; IT IN CPADCN:	JP LDA JZ JP LDA STA LDA MOV NOP NOP NOP NOP NOP NOP JZ JP LDA STAX INX CMP JZ JP LDA STAX INX RET	AKES THE ADC VALU EFERED BY DE CPADCN ADCST VFINAL+1 C, A ADCHB 17 D C CPADN2 CPADN3 ADCLB D D	ES FOR CHRONOPOT AND STORE ; STARTS ADC ; GET FINAL VOLT HIGH BYTE ; STORE IT ; WAIT FOR ADC TO CONVERT ; ; ; ; ; ; ; ; ; ; ; ; ;

MOV C, A ; STORE IT ADCLB LDA GET ADC LOW BYTE STAX D STORE ADC LOW BYTE D JINC MEM ADDR INX CMP С VOLTAGE MORE NEG ; THAN FINAL? RETURN IF LESS NEG RC JMP CP40 ; EXP IS DONE, LEAVE CPADN3: LDA GET ADC LOW BYTE ADCLB STAX D ; STORE IT INX D ; INC MEM ADDR JMP CP40 ; EXP IS DONE, LEAVE ; ADD CHARGE TO ELECTRODE, DAC VALUE & # OF CHG ADD FORM MEM ;HL = HL+2SUBR CHGADM CHGADM: MOV A, M 3 STA DACLB SET DAC LOW BYTE INX н ; MOV A, M ; SET DAC HIGH BYTE DACHB STA ; н INX ; INC MEM ADDR ANI 374 ; MASK DAC HIGH BYTE TO GET # OF CHARGE ADDITIONS RRC , ROTATE TO THE RIGHT RRC POSITION C, A MOV SAVE NUMBER OF CHARGE ; ADDITIONS CGADM1: MVI A, 1 3 STA SW j LDA SWDLY1 GET DELAY VALUE CGADM2: DCR A ; CGADM2 JNZ ;LOOP AROUND TILL IT IS ; DONE MOV A, B ; SW # FOR CHARGING AMPLIFIER SW STA ; DUMP CHARGE TO ; WORKING ELECTRODE CALL SWDLYB ; DELAY С CHECK IF NUMBER OF CHARGE DCR ; ADDITIONS ARE DONE JNZ CGADM1 NO DO IT AGAIN RET ; AND CO HOME i 1 ; THIS ROUTINE COLLECTS DATA FOR CHROPOTENTIOMETRY EXP CP SUBR CP: PUSH H ; SAVE REGS PUSH D 3 PUSH B i PSW PUSH ; н, о LXI ; CLEAR REQ SP DAD ; AND GET STACK POINTER SHLD SSAV ; AND SAVE IT

CPO:	LXI Call LDA CPI	H, VINIT VAPR VAPFLG O	;GET ADDR FOR INIT VOLTAGE ;APPROACH THE VOLTAGE ;GET THE VOLT FLAG ;EQUAL?
	JNZ	CPO	;NO, WAIT
	CALL	READY	TELL US IT'S READY
CP1:	LXI	H, VINIT	GET ADDR FOR INIT VOLTAGE
	CALL	VAPR	APPROACH THE VOLTAGE
	CALL	CHARIN	KEY BOARD TYPED?
	JNC	CP1	IND. KEEP AT DESIRED
			POTENTIAL
	CPI	' 0	: 60?
	JZ	CP2	YES
	CPI	'a	: 602
	JZ	CP2	I YES
	CPI	с. <u>–</u> ′Ь	: BI ANK?
	C7	BLANK	CLEAR VRAM
	JMP	CP1	
CP2	CALL	OFF11	INCORE INTERRURT FROM 11
	DI		DISARIE INT
CP3	CALL	TICP	RET UP TIMER END CHRINNPHT
0.0.		TICMD	OFT TIMED OUTPUT STATUS
		10	OUT 3
	. IN 7	(P3	NOT CORRECT. DO IT AGAIN
		CLOTNA	CIEAD INA
	MUT	6 7 4 7	VOLENK INT VOLENK INT
	CTA	TNITTI DA 1 A	DI AT LEUEL 2
	STA STA	INTILDT14	DI AN LEVEL J
		A 303	i & 4 . Oft a Himd Commands
			JUEI A JUMP CUMMAND
	STA	INTILD+13	
	514	INTILB721	
		n, CP40	PARM ADDR FOR
	SHLD	INTTLB+16	; CHARGE ADD & VOLTAGE ; MEASUREMENT
	LXI	H, CP30	j -
	SHLD	INTTLB+22	;LAST DATUM HAS BEEN TAKEN; ;FINISH UP EXP
	LXI	H, INTCMD	;
	MVI	M, 143	RESET IS FF 3 & 4
	MVI	M, 144	;
	INX	Н	i
	MOV	A, M	INT MASK
	ANI	347	ENABLE LEVEL 3 & 4
	MOV	M, A	
	FI		ENARIE INTERRUPT
CP4:	LXI	H, VINIT	
	CALL	VAPR	KEEP AT INIT VOLTAGE
	JMP	CP4	
CP30-	POP	H	REMOVE CALL
4.94.	LXT	H. CP32	r it has I that V has the Firsten has
		INTTI BAOO	, MR TO CROO ON NEXT INT

.

	LXI	H, DACLIB	i
	LXI	D, ADCLIB	3
	LDA	SWA	;GET SWITCH #
	MOV	B, A	STORE IT IN B
	MVI	A, EDI	1
	STA	INTCMD	3
	FI		
CP31 ·	NOP		3
••••••	NOP		2
			AR MORE NOR'S ARE OMITTED
	IMD	CP31	
ca22.	DI	CF31	
UF JE.			, . OFT BOTENTIAL
		CFADUN	ADD CHADOE
	CALL	CHGADM	ADD CHARGE
	LDA	SWC	i IF = 7
	STA	SW	TURN OFF SWITCH TO AVOID
			; LEAKAGE
	MVI	A, EOI	; GET AN END OF INTERRUPT
			; FLAQ
	STA	INTCMD	; AND TELL THE INTERRUPT
			; CONTROLLER
	EI		3
	RET		RETURN TO DUMMY ROUTINE
CP40:	DI		3
	MVI	A, EDI	GET AN END OF INT FLAG
	STA	INTOMD	AND TELL INT CONTROLLER
	DCX	H	-1 DATA POINT
			I - I DRIA FUINI
CD41.			
6641:			
	CALL		
			TRUTATE ME RIGHT, DIS-U
	SHLD	UA I AN	INUMBER OF DATA
	CALL	TIRSET	RESET TIMER
	MVI	A, EDI	GET AN END OF INT FLAG
	STA	INTCMD	AN TELL INT CONTROLLER
	CALL	CLR IN3	CLEAR IN3
	CALL	CLR IN4	CLEAR IN4
	MVI	A, 377	;SET A RESTART 7
	STA	INTTLB+14	; FOR LEVEL 3
	STA	INTTLB+20	; & 4
	LXI	H, INTCMD	3
	MVI	M, 143	RESET IS FF 3
	MVI	M, 144	; & 4
	INX	н	INT MASK
	MOV	A, M	3
	ORI	30	DISABLE LEVEL 3 & 4
	MOV	м. Д	
		RRAU	CET ODICINAL STACK DOINTED
			AUCH TH CD
			, nuv ml i u ər
	CALL	UNII	J STY ADO 1 TD
	LALL	UPAUP X	IPIX ADD LIB
	LXI	H, PDATAN	GET ADDR UF PARM DATAN
	CALL		STADE IT.

; AND PRINT THE PARM POP PSW ; RESTORE REGS POP B 1 POP D 3 Η POP ; EI ; ENABLE INT RET ; AND GO HOME ï 1 ; ROUTINE TO BRING VOLTAGE BACK SUBR VBACK VBACK: PUSH н ; SAVE REGS PUSH D 3 PUSH PSW i MVI D, 5 **COUNTER** MVI E, 377 1 VBACK1: LXI H, VINIT APPROACH THE INITIAL ; VOLTAGE CALL VAPR 3 VAPFLQ LDA JOET VOLTAGE FLAG CPI 0 ; EQUAL? JNZ VBACK1 ; NOT YET DCR Ε 3 JNZ VBACK1 . LXI H, TIDD ; TIMER DELAY CALL DELAY ; WAIT FOR A WHILE DCR D JECREMENT COUNTER JNZ VBACK1 : POP PSW ; RESTORE REGS POP D : POP H ; RET ï i ï j SUBR CPB CPB: CALL CP ; CHRONOPOTIONMETRY EXP CALL VBACK ; BRING VOLTAGE BACK RET ; AND GO HOME C******* С С CAP. FTN С THIS ROUTINE DIVIDE ONE SET OF AVERAGED DATA С BY ANOTHER С С C. C. LII 7/31/81 С **REVISED 10/24/81** С C******* С DECLARE VARIABLES BYTE INFILE(32), INFIL(32), OTFILE(32), OTFIL1(32) BYTE IDAT(6144), DATE(10), CONC(40), TEMP(10) REAL+8 D1, D2, DPN, DPM, DPD, DPP, DPQ, MF

INTEGER IDATA(3072), IDATA1(3072), IERR, ITABLE(96) INTEGER IERR1, ITABL1(96), ND, I1, I2, ND2 INTEGER VINIT, VFINAL, VSTPO, VSTP1, MO, BLDA1 INTEGER TIPD, TIDD, NO, N1, SWDLY1, SWDLY2, VAPDA, VAPDA1 INTEGER VAPN, VAPN1, N2, SWDLY3, SWDLY4 EQUIVALENCE(INFILE(1), TEST) DATA EX/'EX'/ LUN1=1 LUN2=2LUN3=3 LUN4=4LUN5=5 LUN6=6 С ASK FOR INPUT FILE NAME 5 WRITE(LUN5, 900) 900 FORMAT('\$FILE/FILE1, INPUT FILE: ') С READ THE INPUT FILE NAME READ(LUN5, 905) INFILE 905 FORMAT(32A1) С CHECK IF USER HAS TYPED EXIT TO LEAVE ROUTINE IF (TEST. EQ. EX) GOTO 50 С CALL UNFORM TO OPEN FILE TO READ ENABLE CALL UNFORM(O, IERR, ITABLE, INFILE, O, 1, LUN3) С CHECK FOR ERRORS IF (IERR. NE. 0) GOTO 10 С READ IN THE DATA USING UNFORM NUM=6144 CALL UNFORM(1, IERR, ITABLE, IDATA, NUM) С CHECK FOR ERRORS IF (IERR. NE. 0) GOTO 20 С ASK FOR 2nd INPUT FILE NAME 7 WRITE(LUN5, 906) 906 FORMAT('\$INPUT FILE1: ') С READ THE INPUT FILE NAME READ(LUN5, 907) INFIL 907 FORMAT(32A1) С CALL UNFORM TO OPEN FILE TO READ ENABLE UNFORM(O, IERR1, ITABL1, INFIL, O, 1, LUN2) CALL С CHECK FOR ERRORS IF(IERR1. NE. O)GOTO 11 С READ IN THE DATA USING UNFORM NUM=6144 CALL UNFORM(1, IERR, ITABL1, IDATA1, NUM) С CHECK FOR ERRORS IF (IERR. NE. 0)00T0 20 С CLOSE INPUT FILE CALL UNFORM(2, IERR, ITABLE) С OPEN INPUT FILE AGAIN CALL UNFORM(O, IERR, ITABLE, INFILE, O, 1, LUN3) С READ IN DATA USING UNFORM NUM = 6144CALL UNFORM(1, IERR, ITABLE, IDAT, NUM) IF (IERR . NE. 0) GO TO 20

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С	ASK FOR MULTIPLYING FACTOR
	WRITE(LUN5,908)
908	FORMAT('\$MULTIPLYING FACTOR(CAP/CM**2): ')
С	READ THE FACTOR
	READ(LUN5,909)MF
909	FORMAT(F15.7)
C	ASK FOR OUTPUT FILE
~	WRITE(LUNS, 910)
910	ENDMAT(/ADUTDUT ETLE: /)
C	
C	READ THE OVIEVIETLE NAME
~	
L	UPEN UUIPUI FILE USING ASSIGN
-	CALL ASSIGN(4, UIFILE, 32)
C	SEND HEADER TO OUTPUT FILE
	WRITE(LUN4, 915)INFILE
915	FORMAT('HD ', 32A1)
	WRITE(LUN4,915)INFIL
	ND=IDATA(1)
	NDC=ND-30
С	QET PARAMETERS
	J=3
	DO 110 I=1,10
	DATE(I)=IDAT(J)
	.i=.i+1
110	
	$\frac{115}{115}$
	CONC(I)-IDAT())
	LONG(I)-IDAT(V)
115	
	DU 120 I=1,10
	(EMP(I)=IDA((J)
120	CONTINUE
	VINIT = IDATA(32)
	VFINAL = IDATA(33)
	VSTPO = IDATA(34)
	VSTP1 = IDATA(35)
	MO = IDATA(36)
	BLDA1 = IDATA(37)
	TIPD = IDATA(38)
	TIDD = IDATA(39)
	NO = $IDATA(40)$
	N1 = IDATA(41)
	N2 = IDATA(42)
	SWD(Y) = IDATA(43)
	SUD(Y2 = IDATA(44))
	$\frac{1}{2} = \frac{1}{2} = \frac{1}$
	GRULIT - 10414(407) (JADDA - 10414/47)
	VARUA - 10414(4/) UADNI - 10474(40)
	VMFN = 10A1A(45) (1450A1 - 10ATA(40)
	VAFUAI = IUA(47)
-	VAPNI = IDATA(50)
C	SEND NUMBER OF DATA POINTS TO OUTPUT FILE

000	WRITE(LUN4, 920)NDO
920	IF (VFINAL . GT. VINIT) GD TD 205
С	D5: AVERAGE VOLTAGE FOR CAPACITANCE MEASUREMENT
С	SCAN POSITIVELY
	D5=VINIT*1.0-VSTPO*.5
	D6=-VSTP1*1.0
	GO TO 207
205	D5=VINIT*1. 0+VSTPO*. 5
	D6=VSTP1#1.0
207	VOLT=2. 4988-D5*. 0012207031
C	READ IN THE DATA POINTS CONVERT THEM INTO VOLTAGE
С	AND SEND THEM TO THE OUTPUT FILE
	ND2=ND*3+1
	DO 100 $I=92$, ND2, 3
	I2=IDATA(I)
	IF(I2.GE.O)GO TO 55
	D2=65536. +1. *I2
	GD TO 56
55	D2=I2
56	I1=IDATA(I+1)
	IF(I1.GE.O)GO TO 60
	D1=(65536. +1. +I1)+65536.
	90 TO 61
60	D1=I1*65536.
61	DPN=D1+D2
	DPM=IDATA(I+2)*1.0
	DPO=DPN/DPM
	I2=IDATA1(I)
	IF(I2.GE.O)GOTO 65
	D2=65536. +1. #I2
	GOTO 66
65	D2=12
66	I1=IDATA1(I+1)
	IF(I1.GE.O)GOTO 70
	D1=(65536. +1. +I1)+65536.
	GOTO 71
70	D1=I1*65536.
71	DPN=D1+D2
	DPM=IDATA1(I+2)*1.0
	DPP=DPN/DPM
	DPG=DPO*MF/DPP
	WRITE(LUN4, 925)VOLT, DPG
925	FORMAT('RD', 2E15.7)
С	CALCULATES NEXT VOLTAGE
	D5=D5+D6
	VOLT=2. 4988-D5*. 0012207031
100	CONTINUE.
	WRITE(LUN5, 800)
800	FORMAT(1X, 'PARAMETERS TO 1. TI 2. LP 3. NO OUTPUT ')
	READ(LUN5,805)15
805	FORMAT(15)
	IF(I5 .EQ. 3)00 TO 190

	IF(I5 .EQ. 2)00 TO 125
	LU=LUN5
	GO TO 130
125	LU=LUN6
130	WRITE(LUN5, B10)
810	FORMAT(1X, OUTPUT FILE1: ()
	READ(LUNS, 905)DTFIL1
	CALL ASSIGN(LUN1, DTFIL1, 32)
	WRITE(LUN1, 915) INFILE
	WRITE (LUN1, 920) NDO
	WRITE(LU, 811)INFILE
811	FORMAT(1X, 'FILE', 12X, 32A1)
	WRITE(LU, 812)(DATE(I), I=1, 10)
812	FORMAT(1X, 'DATE', 12X, 10A1)
	WRITE(LUN1, 812)(DATE(I), I=1, 10)
	WRITE(LU, 815)(CONC(I), I=1, 40)
815	FORMAT(1X, 'CONC', 12X, 40A1)
	WRITE(LUN1, 815)(CONC(I), I=1, 40)
	WRITE(LU, 816)(TEMP(I), I=1, 10)
816	FORMAT(11, 'TEMP', 121, 1041)
	WRITE(LUN1, 816)(TEMP(I), I=1, 10)
	VIN=2. 4988-VINIT+ 0012207031
820	FORMAT(1X, 'VINIT', 11X, F15 7, 7X, 'V')
	WRITE(! UN1.820) VIN
	VEI=2 4988-VEINAL # 0012207031
	WRITE((U. 821)VET
821	FORMAT(11, VEINAL (. 101, E15 7.71, (V/)
	WRITE((UN1.821)VET
	WRITE (10, 822) VSTP0
822	FORMAT(1X, (VSTPO(, 11X, 115)
	WRITE(LUN1, 822)VSTPO
	WRITE(10,823)VSTP1
823	FORMAT(11, (VSTP1(, 111, 115)
	WRITE(LUN1, 823)VSTP1
	WRITE (LU. 824) MO
824	FORMAT(1X, 'MO', 14X, 115)
	WRITE(LUN1, 824)MO
	DA3=-(BLDA1-512)+4.8828125
	WRITE(LU, 830)DA3
830	FORMAT(1X, 'BLDA1', 11X, F15 7, 6X, 'mV')
	WRITE(LUN1.830)DA3
	WRITE(10.835)TI
835	FORMAT(1X, 'TIME DELAY', 6X, F15 0, 5X, (u8 ()
	WRITE(LUN1.835)TI
	WRITE(UU.83A)TIPD
836	FORMAT(1X, 'TIPD', 12X, 115)
	WRITE(LUN1, 836)TIPD
	WRITE(LU, 837)TIDD
837	FORMAT(1X, 'TIDD', 12X, 115)
	WRITE(LUN1, 837)TIDD
	WRITE(LU, 845)NO

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845	FORMAT(1X, 'NO', 14X, I15) WRITE(LUN1, 845)NO
	WRITE(LU, 846)N1
846	FORMAT(1X, 'N1', 14X, I15)
	WRITE(LUN1, 846)N1
	WRITE(LU, 853)N2
853	FORMAT(1X, 'N2', 14X, I15)
	WRITE(LUN1, 853)N2
	WRITE(LU, 847)SWDLY1
847	FORMAT(1X, 'SWDLY1', 10X, I15)
	WRITE(LUN1, 847)SWDLY1
	WRITE(LU, 848)SWDLY2
848	FORMAT(1X, 'SWDLY2', 10X, 115)
	WRITE(LUN1, 848)SWDLY2
	WRITE(LU, 854)SWDLY3
854	FORMAT(1X, 'SWDLY3', 10X, 115)
	WRITE(LUN1,854)SWDLY3
	WRITE(LU, 855)SWDLY4
855	FORMAT(1X, 'SWDLY4', 10X, 115)
	WRITE(LUN1, 855)SWDLY4
	DA3=-(VAPDA-512)+4. 8828125
	WRITE(LU, 850) VAPN, DAG
850	FORMAT(1X, 'VAPDA', 6X, I3, ' +', F15, 7, 6X, 'mV')
	WRITE(LUN1, 850) VAPN, DA3
	DA3=-(VAPDA1-512)+4.8828125
	WRITE(LU, 851)VAPN1, DA3
851	FORMAT(1X, 'VAPDA1', 5X, I3, ' +', F15. 7, 6X, 'mV')
	WRITE(LUN1, 851)VAPN1, DA3
	WRITE(LU, 849)MF
849	FORMAT(1X, 'MF(CAP/CM++2)', 3X, F15.7)
	WRITE(LUN1,849)MF
	CALL CLOSE(LUN1)
	GD TD 190
С	NOW CLOSE THE STILL OPEN INPUT FILE USING UNFORM
190	CALL UNFORM(2, IERR, ITABLE)
С	CHECK FOR ERRORS
_	IF (IERR. NE. 0) GOTO 30
	CALL UNFORM(2, IERR1, ITABL1)
С	CHECK FOR ERRORS
-	IF (IERR1, NE, 0) GOTO 30
С	SEND END OF DATA MAKER TO OUTPUT FILE
-	WRITE(LUN4, 930)
930	FORMAT('ED ')
	WRITE(LUN5, 935)
935	FORMAT(' DATA CONVERSION COMPLETE ')
	GOTO 40
10	WRITE(LUN5, 940)
940	FORMAT(' INPUT FILE NOT FOUND ')
. –	GOTO 5
11	WRITE(LUN5, 940)
	GOTO 7
20	WRITE(LUN5,945)
945	FORMAT ('DATA CAN NOT BE READ OUT OF INPUT FILE ')

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GOTO 50 30 WRITE(LUN5, 950) 950 FORMAT(' INPUT FILE CAN NOT BE CLOSED ') С ADN CLOSE THE OUTPUT FILE 40 CALL CLOSE(4) С RETURN TO BEGINNING TO CONVERT ANOTHER FILE QUTD 5 50 CALL EXIT END C******* С С CONCPAD. FTN С THIS ROUTINE CONVERTS BINARY FILE OF С ADC VALUES TO C. G. ENKE & CO. STANDARD DATA FILE С AND A PARAMETER FILE С С C. C. LII 4/22/81 С **REVISED 10/27/81** С C******* С DECLARE VARIABLES BYTE INFILE(32), OTFILE(32), OTFIL1(32) BYTE IDAT(1204), DATE(10), CONC(40), TEMP(10) BYTE NCAP (20) INTEGER IDATA(602), IERR, ITABLE(96), ND INTEGER VINIT, VFINAL, AVEW, BLDA1, CADDMN, COMAX INTEGER CHGN, CHGQ, CPCOR1, CPCOR2, ICAPC1, ICAPC2 INTEGER ICAPDA, ICDN, ICAPSW, SWA INTEGER SWDLY1, SWDLY2, TCENW, TIDDA, TIPD INTEGER VAPDA, VAPDN, VAPDA1, VAPD1N EQUIVALENCE(INFILE(1), TEST) DATA EX/'EX'/ LUN1 = 1 = 3 LUN3 = 4 LUN4 LUN5 = 5 LUN6 = 6 С ASK FOR INPUT FILE NAME WRITE(LUN5, 900) 5 900 FORMAT('\$INPUT FILE: ') С READ THE INPUT FILE NAME READ(LUN5, 905) INFILE 905 FORMAT(32A1) С CHECK IF USER HAS TYPED EXIT TO LEAVE ROUTINE IF (TEST. EQ. EX) GOTO 50 С CALL UNFORM TO OPEN FILE TO READ ENABLE CALL UNFORM(O, IERR, ITABLE, INFILE, 0, 1, LUN3) С CHECK FOR ERRORS IF (IERR. NE. 0) GOTO 10 С ASK FOR OUTPUT FILE 6 WRITE(LUN5, 910) 910 FORMAT('\$OUTPUT FILE: ') С READ THE OUTPUT FILE NAME

READ(LUN5, 905)OTFILE C READ IN THE DATA USING UNFORM NUM=1204 CALL UNFORM(1, IERR, ITABLE, IDATA, NUM) CHECK FOR ERRORS С IF(IERR. NE. 0)GOTO 20 С CLOSE INPUT FILE UNFORM(2, IERR, ITABLE) CALL С OPEN FILE AGAIN UNFORM(O, IERR, ITABLE, INFILE, O, 1, LUN3) CALL READ IN DATA USING UNFORM С NUM=1204 CALL UNFORM(1, IERR, ITABLE, IDAT, NUM) IF(IERR . NE. 0)GO TO 20 С GET PARAMETERS J=3 DO 110 I=1,10 DATE(I)=IDAT(J) J=J+1 110 CONTINUE DO 120 I=1,40 CONC(I)=IDAT(J) J=J+1 120 CONTINUE DO 125 I=1,10 TEMP(I)=IDAT(J) J=J+1 125 CONTINUE DO 130 I=1,20 NCAP(I) = IDAT(J)J=J+1 130 CONTINUE = IDATA(42) VINIT VFINAL = IDATA(43) AVEW = IDATA(44) BLDA1 = IDATA(45)CADDMN = IDATA(46) CCMAX = IDATA(47) CHGN = IDATA(48) CHCQ = IDATA(49) CPCOR1 = IDATA(50) = IDATA(51) CPCOR2 ICAPC1 = IDATA(52) ICAPC2 = IDATA(53) ICAPDA = IDATA(54) ICDN = IDATA(55) ICAPSW = IDATA(56) SWA = IDATA(57) SWDLY1 = IDATA(58) SWDLY2 = IDATA(59)TCENW = IDATA(60)TIDDA = IDATA(61)

TIPD

= IDATA(62)

	VAPDA = IDATA(63)
	VAPDN = IDATA(64)
	VAPDA1 = IDATA(65)
	VAPD1N = IDATA(66)
	BLDA3 = IDATA(67)
	IF (TIDDA . GE. 0) GO TO 126
	TIDD=65536. +1. *TIDDA
	GO TO 127
126	TIDD=TIDDA
c	OPEN OUTPUT FILE USING ASSIGN
127	CALL ASSIGN(LUN4, OTELE, 32)
C	SEND HEADER TO OUTPUT ETLE
U	URITE (ILINA. 915) INFILE
915	FURMAT('HD '. 32A1)
/10	ND=TDATA(1)
c	CEND NUMBER OF DATA BOINTS TO OUTBUIT FILE
U	JEND NUMBER OF DATA FOINTS TO OVIEVI FILE
920	CODMAT(/MI (115)
720	PEAD IN THE BATA BOINTS CONDERT THEM INTO US TAKE
	AND BEND THEM TO THE OUTBUT ETLE
C	AND SEND INEN ID INE DUIFUI FILE
	111-0. U DO 100 I-02 ND
	DU 100 I=72,ND
	A=10ATA(1) D=0.4000 A# 0040007004
	5=2.4788-A*.001220/031
	WR11E(LUN4, 723)111,8
423	FURMAT('RD', 2E15.7)
	11=11+11DD#2.0#10.##(-6)
100	
	WRITE(LUN5, 800)
800	FURMAT(1X, 'PARAMETERS TO 1. TI 2. LP 3. NU UUTPUT ')
	READ(LUN5, 803) 15
805	FURMAT(15)
	IF(15.EQ. 3)QD TD 190
	WRITE(LUN5, 806)
806	FORMAT(1X, 'CAPACITANCE FOR CHG ADD/ELECTRODE',)
	WRITE(LUN5, 807)
807	FORMAT(1X, 'AREA(uf/CM**2): ')
	READ(LUN5, 808)C1
808	FORMAT(F15.7)
	IF(I5 .EQ. 2)QO TO 135
	LU=LUN5
	GO TO 140
135	LU=LUN6
140	WRITE(LUN5, 810)
810	FORMAT(1X, 'OUTPUT FILE1: ')
	READ(LUN5,905)OTFIL1
	CALL ASSIGN(LUN1, DTFIL1, 32)
	WRITE(LUN1, 915)INFILE
	WRITE(LUN1, 920)NDO
	WRITE(LU, 811)INFILE

811 FORMAT(1X, 'FILE', 12X, 32A1)

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	WRITE(LU,812)(DATE(I),I=1,10)
812	FORMAT(1X, 'DATE', 12X, 10A1)
	WRITE(LUN1,812)(DATE(I),I=1,10)
	WRITE(LU, 815)(CONC(I), I=1, 40)
815	FORMAT(1X, 'CONC', 12X, 40A1)
	WPTTE(UN1, 915)(CONC(T), T=1, AO)
	WRITE/LU 914)/TEMP/I) I-1 10)
014	WRITELEVICIO/(ICHENI/17/1-1/10/
010	FURNAILIA, 'IENF'/IEA/IVAI/
	WR1(E(LUNI)810/((EMP(1))1=1,10)
	WR11E(LU/01/)(NUAF(1)/1=1/2U)
817	FURMAI(1X, 'NCAP', 12X, 20A1)
	WRITE(LUN1, 817)(NCAP(I), I=1, 20)
	WRITE(LU, 818)C1
818	FORMAT(1X, 'CAP/AREA', 8X, F15. 7, 8X, 'uf/CM##2')
	WRITE(LUN1,818)C1
	TIENT=2. *TIDD*10. **(-6)
	CURNT=C1*(CHGN*CHGQ*1.0)*.0048828125/TIENT
	WRITE(LU, 819)CURNT
819	FORMAT(1X, 'CURRENT', 9X, F15. 7, 8X, 'uA/CM**2')
	WRITE(LUN1,819)CURNT
	VIN=2. 4988-VINIT*. 0012207031
	WRITE(LU, 820)VIN
820	FORMAT(1X, 'VINIT', 11X, F15, 7, 15X, 'V')
	WRITE(LUN1, 820)VIN
	VFI=2.4988-VFINAL*.0012207031
	WRITE(LU, 821)VFI
821 ·	FORMAT(1X, 'VEINAL', 10X, F15 7, 15X, 'V')
	WRITE(LUN1, 821)VFI
	WRITE(11,822)AVEW
822	FORMAT(11, 'AVEW', 121, 115)
	WRITE(LUN1.822)AVEW
	NA3=- (RI NA1-512) +4 8928125
	UPITE////.023/043
077	WRIIC(LU) OLG/DHG CODMAT/19 /BIDA1/ 119 C1E 7 149 /-U/\
OKJ	FURMAY(IX) BLDA1'/IIX/FIJ.//I4X/ 'MV'/
	DA3=-(BLDA3-312)*4.8828123
	WRITE(LU, 828)DA3
858	FORMAT(1X, 'BLDA3', 11X, F15. 7, 14X, 'mV')
	WRITE(LUN1, 828)DA3
	WRITE(LU, 824)CADDMN
824	FORMAT(1X, 'CADDMN', 10X, 115)
	WRITE(LUN1, 824)CADDMN
	DA3=-(CGMAX-512)*4.8828125
	WRITE(LU, 825)DA3
825	FORMAT(1X, 'CGMAX', 11X, F15.7, 14X, 'mV')
	WRITE(LUN1,825)DA3
	WRITE(LU, 826)CHGN
826	FORMAT(1X, 'CHQN', 12X, I15)
	WRITE(LUN1,826)CHGN
	WRITE(LU, 827)CHQQ
827	FORMAT(1X, 'CHQQ', 12X, I15)
	WRITE(LUN1,827)CHGQ
	WRITE(LU, 830)CPCOR1

830	FORMAT(1X, 'CPCOR1', 10X, I15)
	WRITE(LUN1,830)CPCOR1
	WRITE(LU, 831)CPCOR2
831	FORMAT(1X, (CPCOR2(, 10X, 115)
	NR1 (E(EVR1) 001/0FUURE
000	WR1/E(LV/032/104F01
97 5	PURMAT(1X; 'ICAPC1'; 10X; 115)
	WR1TE(LUN1, 832) ICAPC1
	WRITE(LU,833)ICAPC2
833	FORMAT(1X, 'ICAPC2', 10X, I15)
	WRITE(LUN1,833)ICAPC2
	DA3=-(ICAPDA-512)*4.8828125
	WRITE(LU, 840) ICDN, DA3
840	FORMAT(1X, 'ICAPDA', 5X, I3, ' *', F15, 7, 14X, 'mV')
	WRITE (LUN1, 840) LCDN, DAG
	WETTE (ILL. 841) TCAPSU
941	CODMAT/19 /10A604/ 109 118)
041	CURTERITAL CALVOADOU
	WRITE(LUNI)841)ICAF8W
	WRI(E(LU)842)8WA
842	FORMAT(1X, 'SWA', 13X, 115)
	WRITE(LUN1,842)SWA
	WRITE(LU, 847)SWDLY1
847	FORMAT(1X, 'SWDLY1', 10X, I15)
	WRITE(LUN1,847)SWDLY1
	WRITE(LU, 848)SWDLY2
848	FORMAT(1X, 'SWDLY2', 10X, 115)
	WRITE(LUN1, 848)SWDLY2
	WRITE(LU, 849)TCENW
849	FORMAT(1X, 'TCENW', 11X, 115)
	WRITE (I UN1. 849) TOENH
	WRITE(LU, 850)TIDD
850	ENPMAT(11, (TIDD), 121, E15 1, 141, (us())
000	LIDITE/ILINI GEALTIDD
	W(1)E(LUN1)000//100
051	WRITE(LU/001/TIFU)
821	PURMAI(1X, 11PD', 12X, 115)
	WRITE(LUN1, 851)TIPD
	TI=2*TIDD
	WRITE(LU, 852)TI
852	FORMAT(1X, 'TIME DELAY', 6X, F15. 1, 14X, 'us')
	WRITE(LUN1,852)TI
	DA3=-(VAPDA-512)*4.8828125
	WRITE(LU, 853)VAPDN, DA3
853	FORMAT(1X, 'VAPDA', 6X, I3, ' +', F15, 7, 14X, 'mV')
	WRITE (LUN1.853) VAPDN. DA3
	DA3=-(VAPDA1-512)+4 8828125
	WEITE(III. 854) VARDIN, DAG
954	CTOMAT/19. (UADDA1/.59.13 / #/ E15 7.149./eU/)
0.74	LIDITE/LUNI GEALUADDIN DAD
	MILIELLUNIJOJY/MEDIN/DAJ
	URLL ULUGE(LUNI/
~	
	NUW CLUSE THE STILL UPEN INPUT FILE USING UNFORM
140	CALL UNFORM(2, IERR, ITABLE)
C	CHECK FOR ERRORS

•

	IF (IERR. NE. 0) GOTO 30
С	SEND END OF DATA MAKER TO OUTPUT FILE
	WRITE(LUN4, 930)
930	FORMAT('ED ')
	WRITE(LUN5,935)
935	FORMAT(' DATA CONVERSION COMPLETE ')
	QDTD 40
10	WRITE(LUN5,940)
940	FORMAT(' INPUT FILE NOT FOUND ')
	QOTO 5
20	WRITE(LUN5,945)
945	FORMAT('DATA CAN NOT BE READ OUT OF INPUT FILE ')
	GOTO 50
30	WRITE(LUN5,950)
950	FORMAT(' INPUT FILE CAN NOT BE CLOSED ')
C	ADN CLOSE THE OUTPUT FILE
40	CALL CLOSE(4)
С	RETURN TO BEGINNING TO CONVERT ANOTHER FILE
	QOTO 5
50	CALL EXIT
	END

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